



LP Wizard Release Notes

Software Version 10.3 Update 1

February 2011

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Corrected Problems

Problems Fixed in This Release

This section describes problems fixed for the LP Wizard 10.3 Update 1 release.

LP Wizard

CAD Export

- **dts0100745878 – CAD Export** - Thermal TAB for both SMT QFN's and SON's no longer build in Allegro - works in V10.1.1
- **dts0100748117 – CAD Export** - CAD Output for Allegro broken for LP V10.3 and V10.2.1 - will not complete
- **dts0100748197 – CAD Export** - LP V10.3 Expedition CAD Output Error during *_Cell.hkp file Import
- **dts0100746757 – CAD Export** - CAD Output for Allegro 16.3 EXTENTS issue has re-emerged
- **dts0100746588 – CAD Export** - QFN Silkscreen in Land Patterns is generated wrong for Allegro 16.3 - all corners 90 degrees off.
- **dts0100746490 – CAD Export** - Allegro .pad file ERROR when using Rounded Corners (shape symbol script) to create a chip resistor land pattern.

Problems Fixed in Prior Release

This section describes problems fixed for the LP Wizard 10.3 release.

LP Wizard

CAD Export

- **dts0100724292 – CAD Export** - Incorrect rotation for Lands On Assembly for Allegro

Calculator

- **dts0100740181 – Calculator** - Solder Mask & Paste Mask shapes change to Round Corner if use the Custom Settings by oversize the Solder Mask and/or Paste Mask

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- **dts0100732021 – Calculator** - SOT143 Corner Radius from Calculator Settings is not implemented on pad when set polarity is performed
 - **dts0100739028 – Calculator** - SIP and DIP TH packages won't compile when +/- Tol format is selected. No error is displayed. Missing "K" entry
 - **dts0100739214 – Calculator** - CAD Tools > Altium: does not retain layer re-mapping preferences after save, resets to default values
 - **dts0100710520 – Calculator** - QFN output for Boardstation generates in incorrect thermal pad

Librarian

- **dts0100740773 – Librarian** - PLB "Description" attribute discrepancies

Changes and Enhancements

Librarian

- **dts0100735801 – Librarian** - LP Wizard 10.2 : Flexlm version vendor daemon too old

Known Problems and Workarounds

- **Problem: dts0100742991** - For a THRU Hole Padstack; Antipad information is missing in exported ASCII geometry data for Board Station from LPWizard

Note: Defect resolution postponed due to code freeze for pending release

- **Problem: dts0100743411** - LP Printable datasheet has mis-matched dimension references from Figures to Tables. The CHIP datasheet Fig. 5 Anom and Bnom in dim area is not A and B in drawing area but rather Lnom and Bnom.

Note: Defect resolution postponed due to code freeze for pending release

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