

## Practical Loop Compensation for Switching Power Converters

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2/23/04

Date 2004

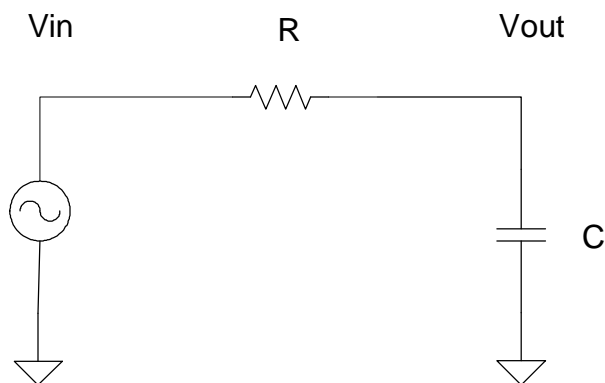
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 TEXAS INSTRUMENTS

- Agenda
  - Review a Simple RC Filter Network Transfer Function
  - Review a Second Order LC Network Transfer Function
  - Simplified Control Block Diagram for Power Converters
  - Continuous Conduction Mode (CCM)
  - Discontinuous Conduction Mode (DCM)
  - Compensation Examples
    - Voltage Mode Buck Converter
    - Voltage Mode Buck Converter with Voltage Feed Forward (VFF).
    - Peak Current Mode Forward Converter

## RC Filter Network



- Simple RC Network is Used to Filter Out High Frequency Noise
  - First Order Filter with Single Pole Roll off at -20dB/decade after  $f_p$

$$G_f(s) = \frac{\Delta V_{out}}{\Delta V_{in}} = \frac{1}{(sRC + 1)} \quad f_p = \frac{1}{2\pi RC}$$

# Bode Plot Review

- Bode Plots are one of the simplest tools that can be used to evaluate transfer functions
  - Zeros add a 20dB per decade change in gain and add 45 degrees of phase per decade
    - At the zero frequency there is 45 degrees of added phase and a added positive 3dB of gain
    - Each zero will add a maximum of 90 degrees of phase
  - Poles add a -20dB per decade change in gain and add -45 degrees of phase per decade
    - At the pole frequency there is -45 degrees of added phase and a added -3dB gain
    - Each pole will add a maximum of -90 degrees of phase

$$G_{dB}(f) = 20 \log \left( \left| \frac{(2 * j * \pi * f * t_z + 1)}{(2 * j * \pi * f * t_p + 1)} \right| \right)$$

$$f_p = \frac{1}{2 * \pi * t_z} \qquad f_z = \frac{1}{2 * \pi * t_z}$$

# Review Complex Numbers

Adding complex numbers

$$(a + jb) + (c + jd) = j(b + d) + (a + b)$$

When multiplying or dividing complex numbers it is best to convert them to vectors first

$$\text{Magnitude} = \sqrt{(\text{Real})^2 + (\text{Imaginary})^2}$$

$$\text{Phase} = \tan^{-1} \left( \frac{\text{Imaginary}}{\text{Real}} \right)$$

Magnitude  $\angle$  Angle

# Review Vector Manipulation

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Multiplying Vector

$$M1\angle\Theta1 * M2\angle\Theta2 = (M1 * M2)\angle(\Theta1 + \Theta2)$$

Dividing Complex Numbers

$$\frac{M1\angle\Theta1}{M2\angle\Theta2} = \left( \frac{M1}{M2} \right) \angle(\Theta1 - \Theta2)$$

# RC Filter Small Signal Characteristics

## Transfer Function

$$\frac{\Delta V_{out}}{\Delta V_{in}} = \frac{1}{(SRC + 1)}$$

## Magnitude

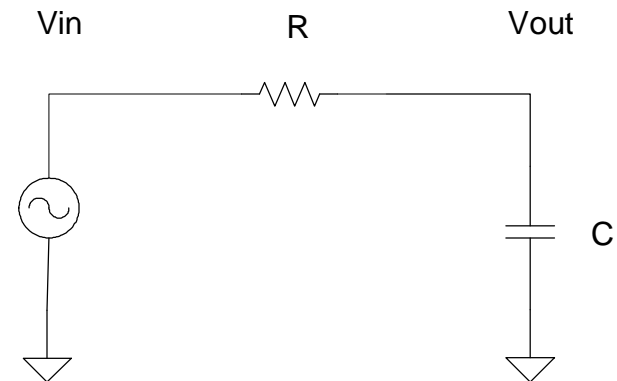
$$|G(f)| = \frac{1}{\sqrt{(1)^2 + (2\pi fRC)^2}}$$

## Phase

$$\text{Phase} = -\tan^{-1}\left(\frac{2\pi fRC}{1}\right)$$

## dB Gain

$$G_{dB}(f) = 20 * \log(|G(f)|)$$

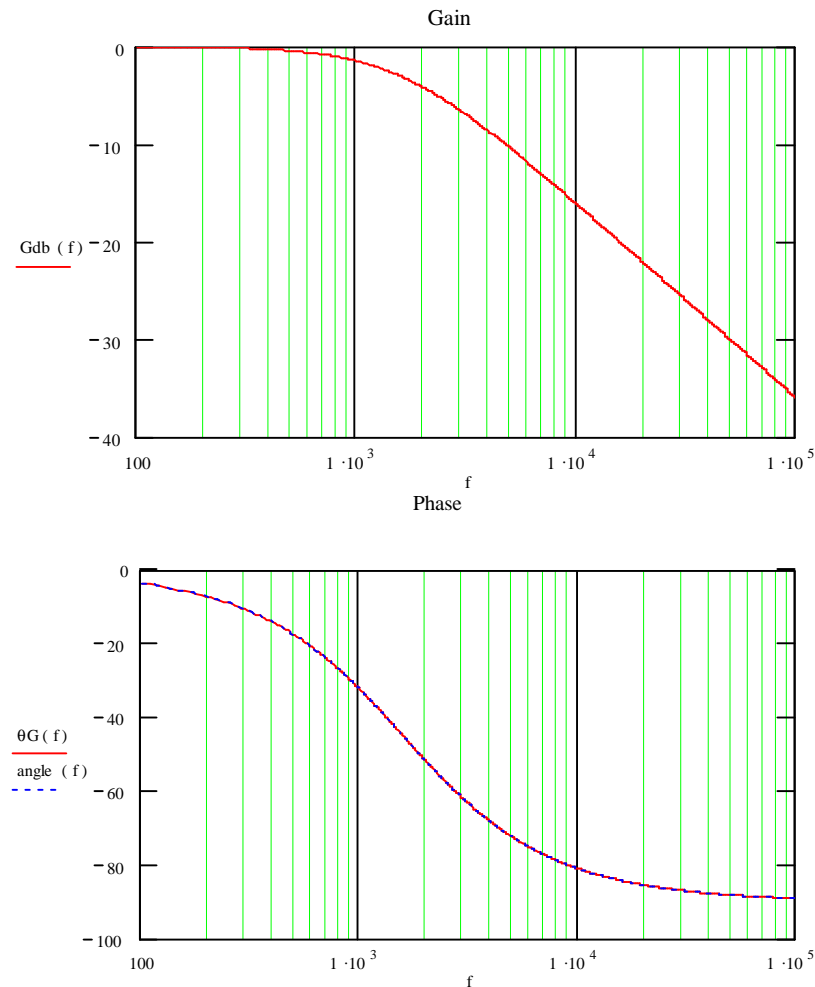


# Bode Plot of RC Filter Network

- Bode Plot of RC filter with an R of 100 ohm and a C of 1 uF.
  - This puts a pole roughly 1.6 kHz
    - $G_{db}(f_p) = -3\text{db}$
    - Phase = -45 degrees
  - A decade later at 16.5k Hz the phase lag is -90 degrees

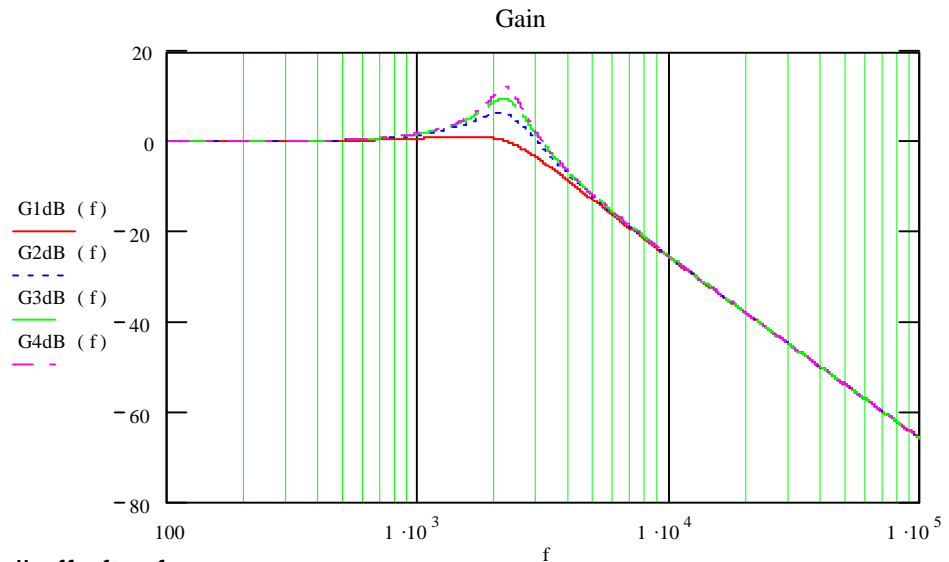
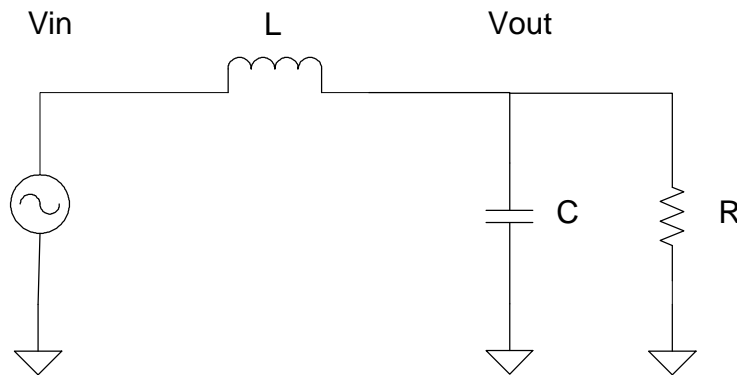
$$G(f) = \frac{\Delta V_{out}}{\Delta V_{in}} = \frac{1}{(SRC+1)} \quad f_p = \frac{1}{2\pi RC}$$

$$G_{dB}(f) = 20 * \log(|G(f)|)$$





# LRC Filter Network



- LRC Filter Network Transfer Function
  - Second order filter -40dB per decade gain roll off after fp.

$$Gf(s) = \frac{\Delta V_{out}}{\Delta V_{in}} = \frac{1}{1 + \frac{s}{w_p * Q} + \left(\frac{s}{w_p}\right)^2}$$

$$w_p = \frac{1}{\sqrt{LC}}$$

$$f_p = \frac{1}{2\pi\sqrt{LC}}$$

$$Q = \frac{R}{w_p * L}$$



# LRC Filter Small Signal Characteristics

Behind Your Designs

Transfer Function

$$Gf(f) = \frac{1}{1 + \frac{s}{w_p * Q} + \left(\frac{s}{w_p}\right)^2} \quad w_p = \frac{1}{\sqrt{LC}} \quad Q = \frac{R}{w_p * L}$$

Magnitude

$$|Gf(f)| = \frac{1}{\sqrt{\left(1 - \left(\frac{2\pi f}{w_p}\right)^2\right)^2 + \left(\frac{2\pi f}{w_p * Q}\right)^2}}$$

Phase

$$\text{Phase} = -\tan^{-1} \left( \frac{\frac{2\pi f}{w_p Q}}{1 - \left(\frac{2\pi f}{w_p}\right)^2} \right)$$

dB Gain

$$GdB(f) = 20 * \log(|G(f)|)$$

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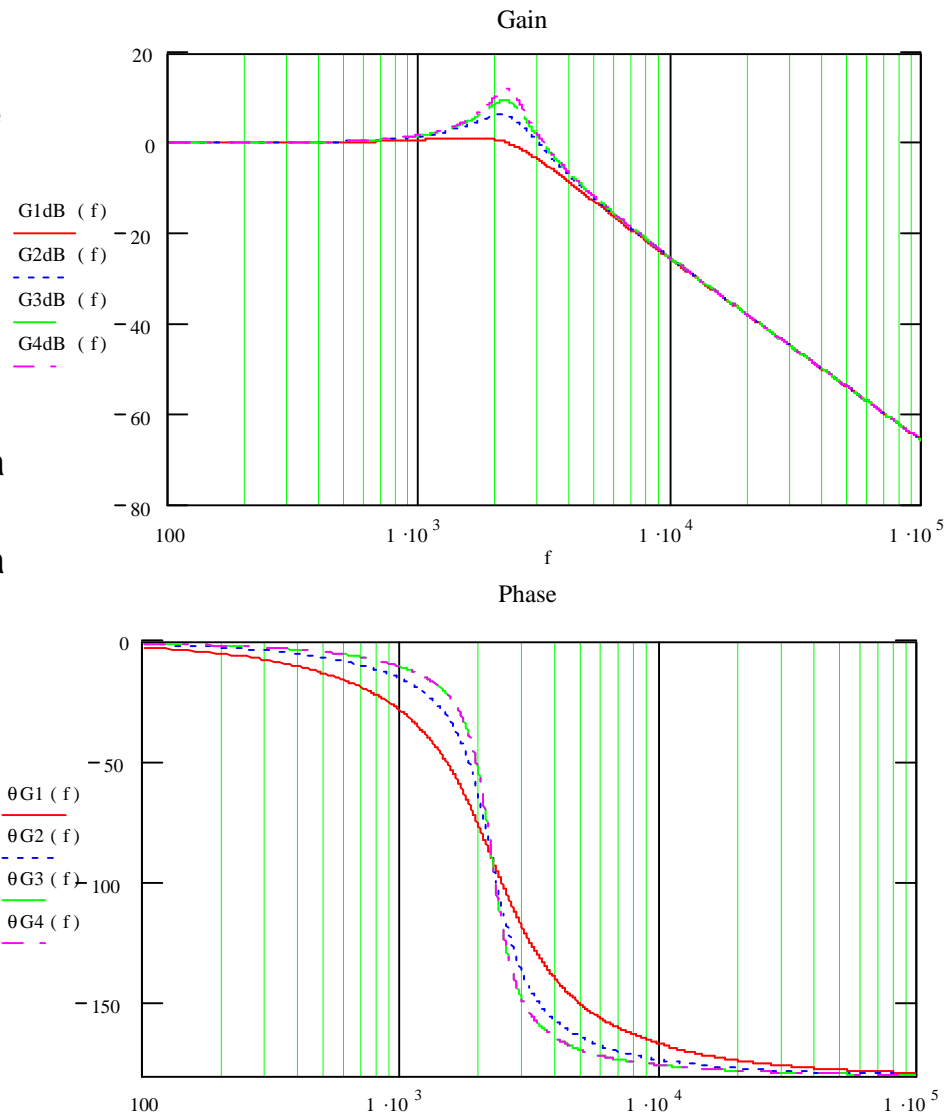


## Review LCR Filter Response with Different Q Factors

- As Q increases so does the peak magnitude
  - G1dB(f) is the LCR filter response with a Q of 1
  - G4dB(f) is the LCR filter response with a Q of 4
- As Q increases so does the steepness of the phase roll off.
  - θG1(f) is the LCR filter response with a Q of 1
  - θG4(f) is the LCR filter response with a Q of 4

$$Gf(f) = \frac{1}{1 + \frac{s}{w_p * Q} + \left(\frac{s}{w_p}\right)^2} \quad Q = \frac{R}{w_p * L}$$

Note the only thing changing in the circuit is the load impedance RL



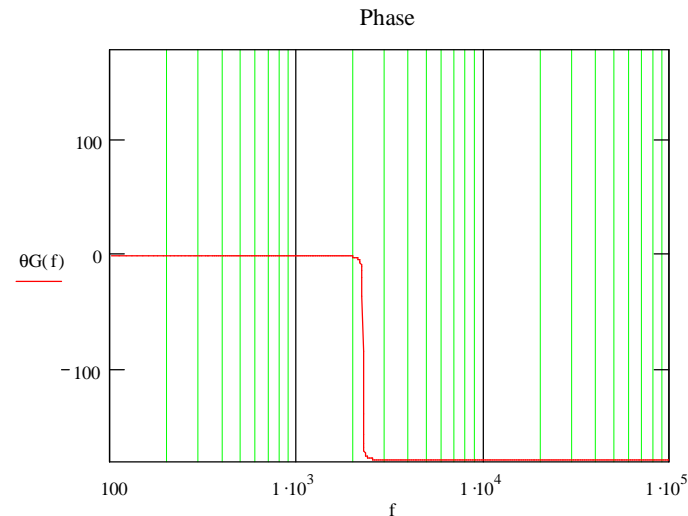
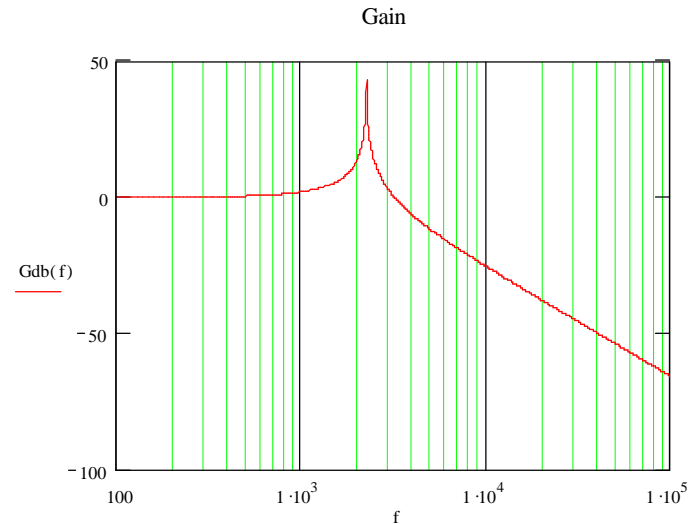
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# Bode Plot of LRC Filter Network

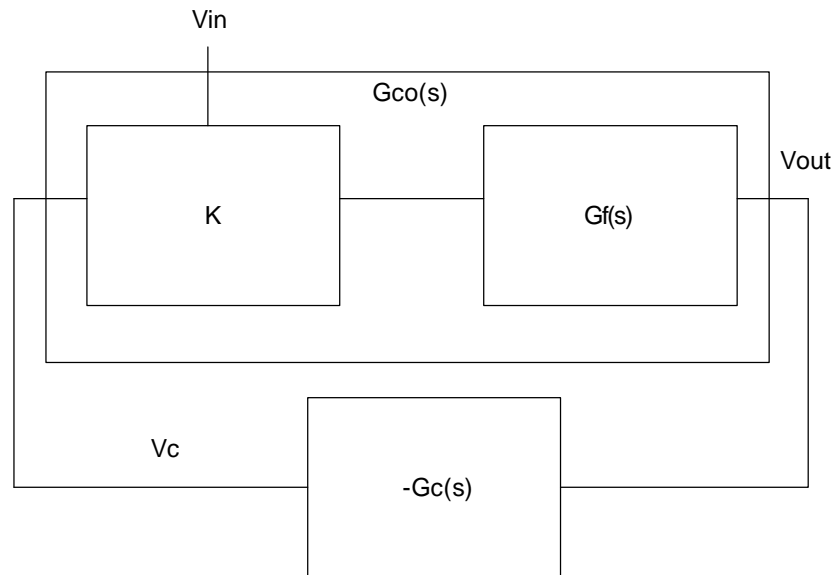
- Bode Plot of LRC filter with an L of 50  $\mu$ H, R of 100 ohm and a C of 100  $\mu$ F.
  - This puts a double pole roughly 2.25 kHz
    - $Q = 142$
  - Phase roll off the double pole is very steep

$$G(f) = \frac{1}{1 + \frac{s}{w_p * Q} + \left(\frac{s}{w_p}\right)^2} \quad f_p = \frac{1}{2\pi\sqrt{LC}}$$

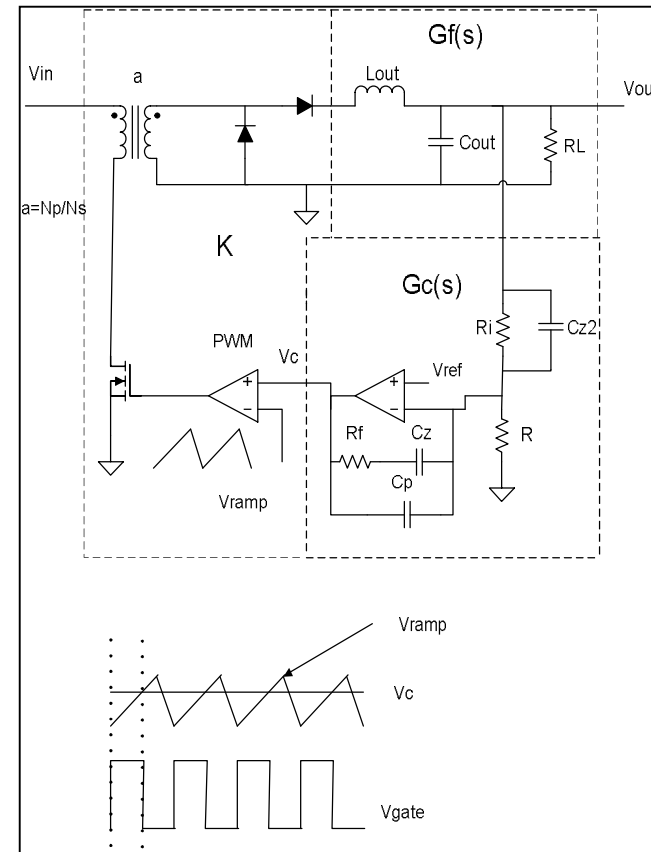
$$GdB(f) = 20 * \log(|G(f)|)$$



## Simplified Loop Gain Control Block Diagram for a Power Converter



$$T(s) = K * G_f(s) * (-G_c(s)) = -G_{co}(s) * G_c(s)$$

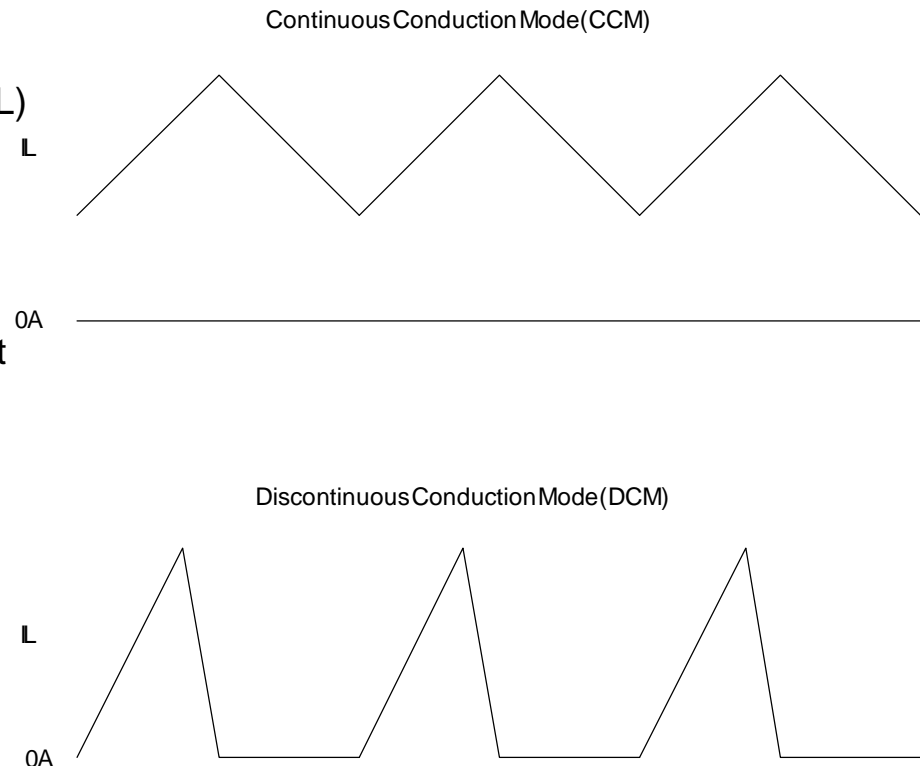


## Simplified Loop Gain Control Block Diagram

- The simplified control block diagram can be used to control several different types of power converters
  - Voltage Mode Control
  - Voltage Mode Control with Voltage Feed Forward (VFF)
  - Peak Current Mode Control
- It is just a matter of correctly setting up the transfer function control blocks
  - $K$  is the modulator transfer function and represents the average voltage that is applied to the power stages output filter ( $G_f(s)$ ).
  - $G_c(s)$  is the compensation transfer function
  - The control to output gain  $G_{co}(s)$  is the combination of  $K$  and  $G_f(s)$
- This control block diagram is used to compensate the feedback loop gain
  - The models that are used in this presentation are based on ideal components
  - Bode plots can be used to compensate the feedback loop
- The voltage feed back loop  $T(s)=K*G_f(s)*(-G_c(s))$

## CCM and DCM Power Converter Operation

- Continuous Conduction Mode (CCM)
  - The Inductor has continuous current ( $I_L$ )
  - Used in forward/buck converters to meet output ripple voltage requirements
- Discontinuous Conduction Mode (DCM)
  - The inductor has discontinuous current ( $I_L$ )
  - This is mostly used in flyback converters to make voltage loop compensation easier.
- The current mode of operation will have an affect on the control to output gain of the power converter  $G_{co}(s)$ .

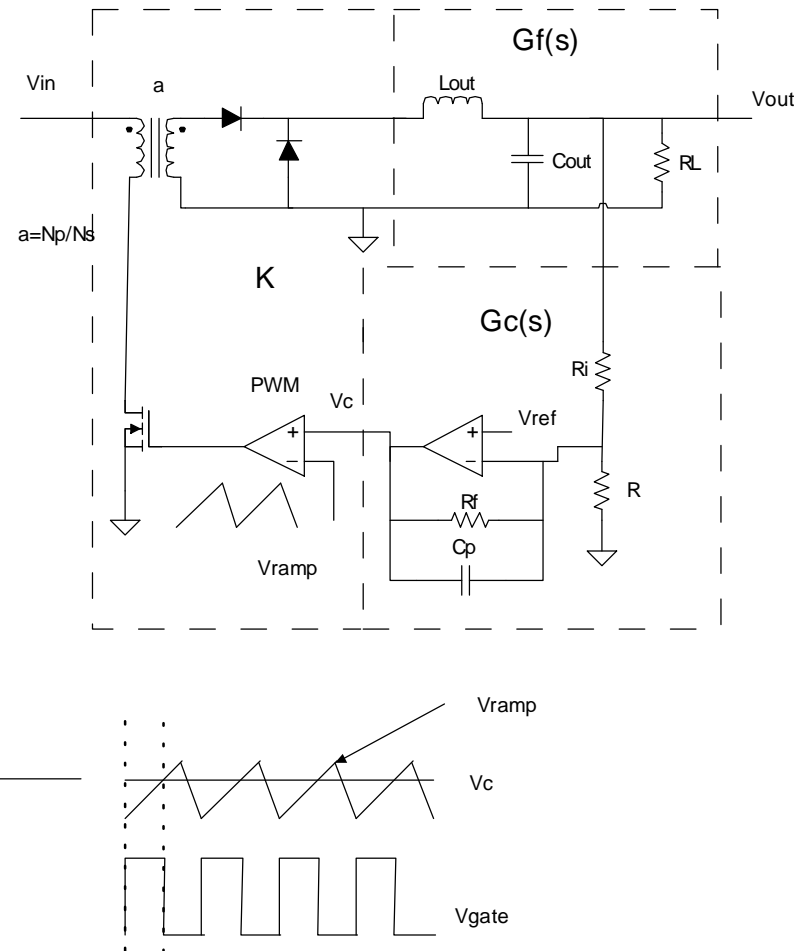
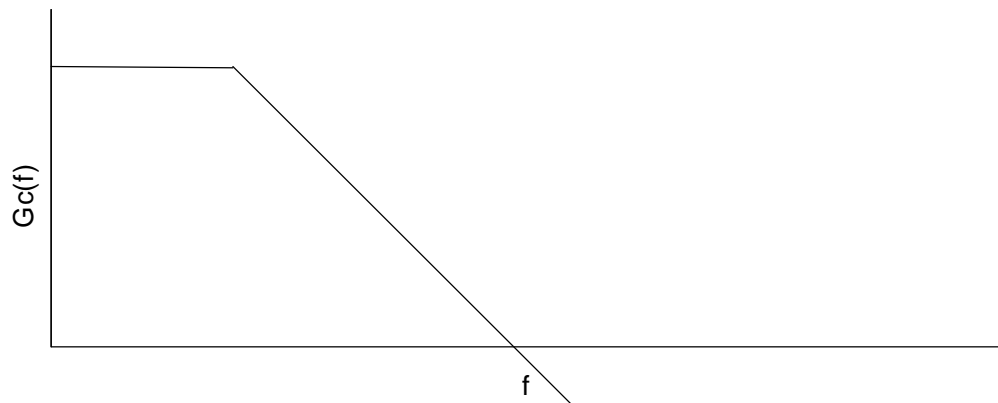


$$G_{co}(s) = \frac{\Delta V_{out}}{\Delta V_c} = K * G_f(s)$$

# Compensation Networks $G_c(s)$

- Type 1 Compensation  $G_f(s)$

$$G_c(f) = \frac{\Delta V_1}{\Delta V_{out}} = \frac{R_f}{R_i * ((s * R_f * C_p + 1))}$$

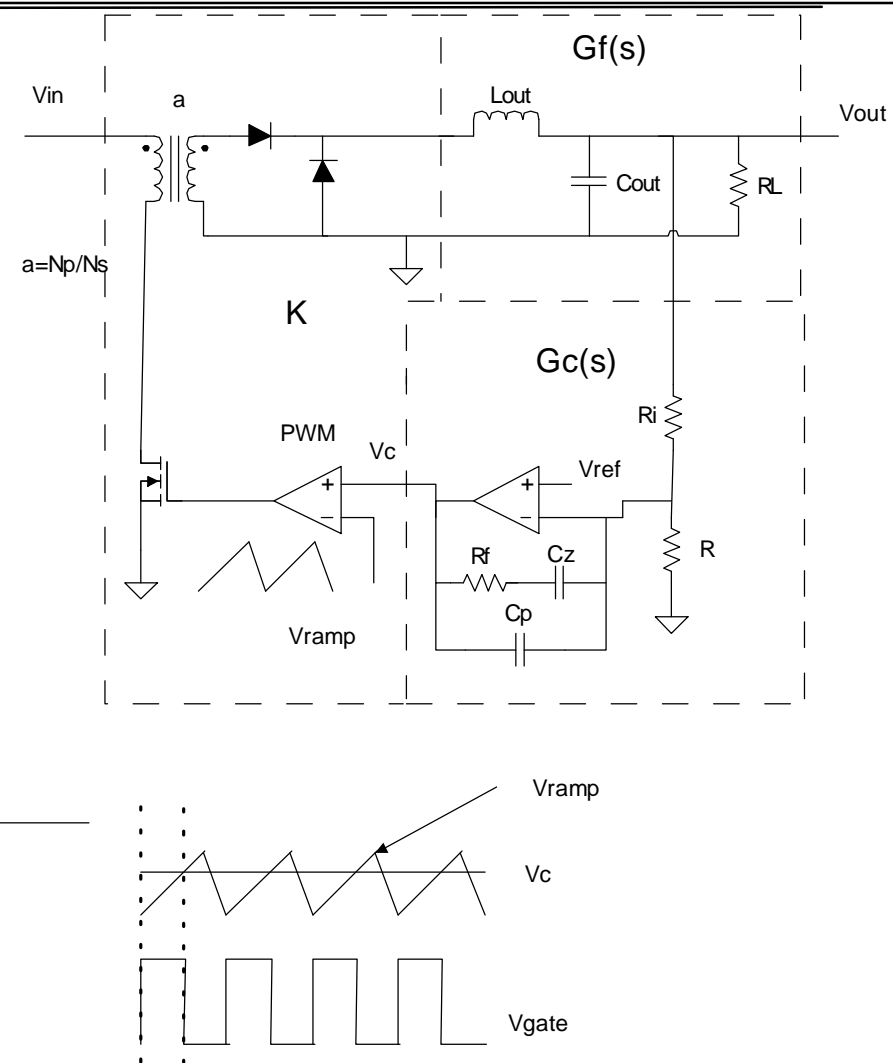
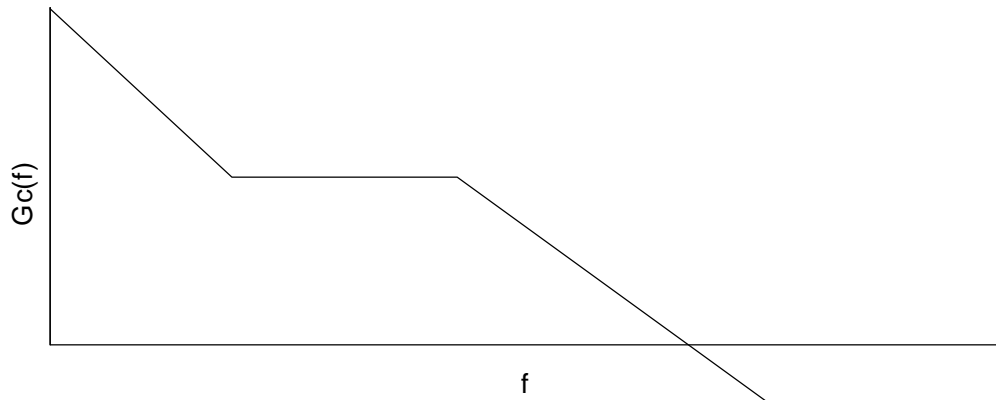




# Compensation Networks $G_c(s)$

- Type 2 Compensation  $G_f(s)$

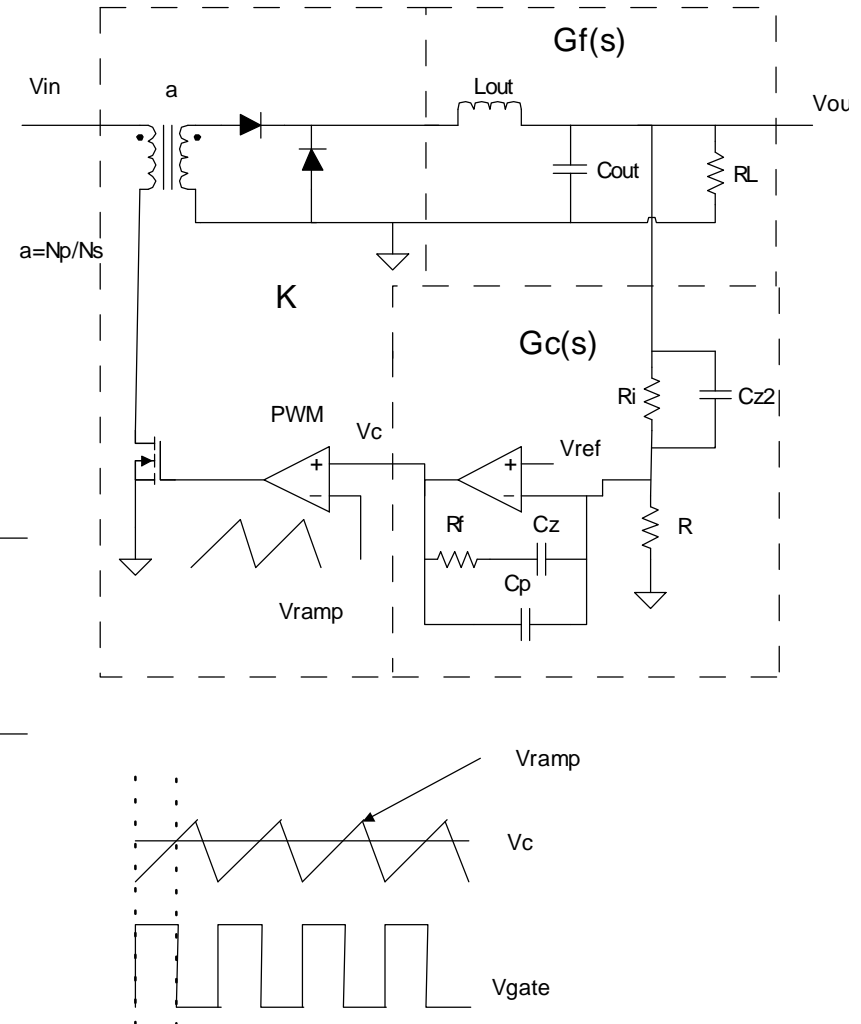
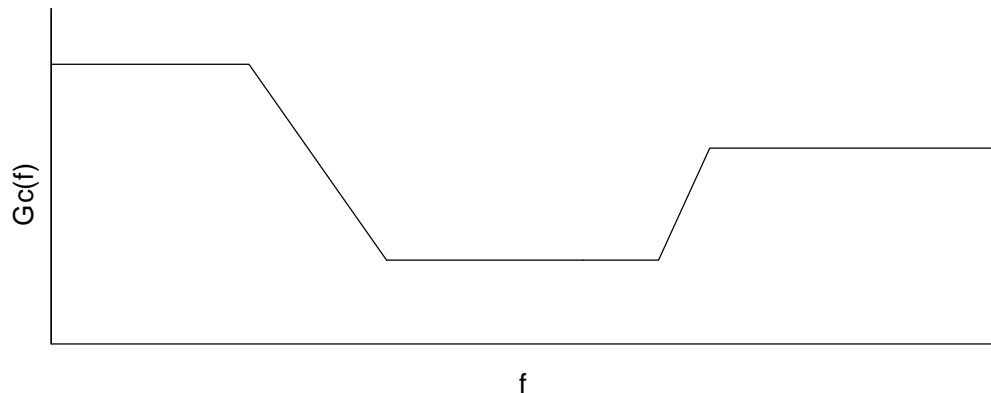
$$G_c(f) = \frac{\Delta V_1}{\Delta V_{out}} = \frac{(s * R_f * C_z + 1)}{s * R_i * (C_p + C_z) ((s * R_f * C_p + 1))}$$



# Compensation Networks $G_c(s)$

- Type 3 Compensation  $G_f(s)$

$$G_c(f) = \frac{\Delta V_c}{\Delta V_{out}} = \frac{(s \cdot R_f \cdot C_z + 1) \cdot (s \cdot R_i \cdot C_z + 1)}{s \cdot R_i \cdot (C_p + C_z) \cdot (s \cdot R_f \cdot C_p + 1)}$$



## PM and GM

- Phase Margin (PM) is the difference in phase from the voltage loop  $T(s)$  and 180 degrees at the loop crossover frequency.
  - Gain at  $f_c$  is  $20\log(T(s))=0\text{dB}$
  - At DC the phase of  $(T(s))$  is 180 degrees out of phase. 180 degrees out of phase would be 0 degrees or 360 degrees
  - $\text{PM} = \text{Phase} - 0 \text{ degrees or } 360 \text{ degrees} - \text{Phase}$ 
    - >Use the smaller of the two
  - The control loop needs to be designed for 45 degrees of phase margin at crossover.
  - The loop is generally designed to crossover below one sixth of the switching frequency. For this design example we chose to crossover at 1/10 of the switching frequency
- Gain Margin
  - Is the gain when the voltage loop  $(T(s))$  is 180 degrees out of phase
  - The control loop needs to be designed for a -6dB gain margin to ensure loop stability

- Power Converter Specifications
  - $V_{in} = 12 \text{ to } 24 \text{ V}$
  - $V_{out} = 5 \text{ V}$
  - $V_{ref} = 2.5 \text{ V}$  internal reference used by the compensation  $G_c(f)$
  - Switching Frequency  $f_s = 100 \text{ kHz}$
  - $L_{out} = 5 \text{ uH}$
  - $C_{out} = 1000 \text{ uF}$  with an ESR of  $5 \text{ mohm}$
  - $P_{out} = 100\text{W}$  / Load Impedance  $(R_L) = 0.25 \text{ ohm}$
  - PWM Ramp ( $V_{ramp}$ ) =  $5\text{V}$

# Voltage Mode Forward/Buck Converter CCM

- Define the control block transfer functions

- Filter is required to filter chopped input voltage

$$G_f(s) = \frac{(s * C_{out} * ESR + 1)}{1 + \frac{s}{w_p * Q} + \left(\frac{s}{w_p}\right)^2}$$

- Modulator Transfer Function

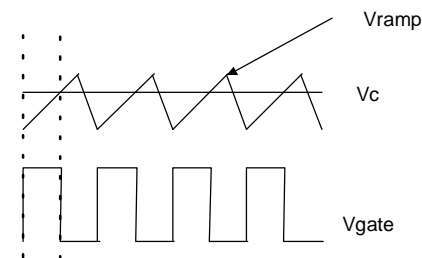
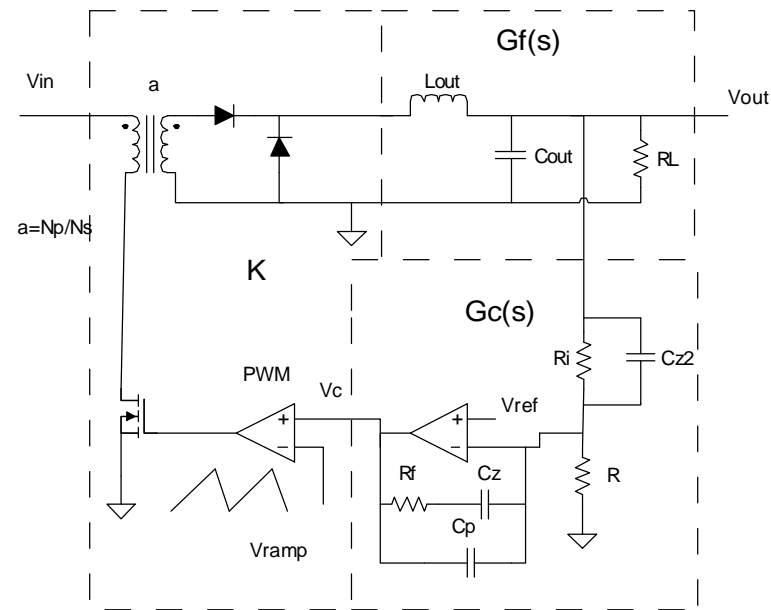
$$K = \frac{1}{a} \frac{V_{in}}{V_{ramp}}$$

- Control to Output Transfer Function

$$G_{co}(s) = K * G_f(s)$$

- Type 3 Compensation Feedback

$$G_c(f) = \frac{\Delta V_c}{\Delta V_{out}} = \frac{(s * R_f * C_z + 1) * (s * R_i * C_{z2} + 1)}{s * R_i * (C_p + C_z) * ((s * R_f * C_p + 1))}$$



## Evaluate the $G_{co}(f)=K*G_f(f)$ Transfer Function

With a bode plot it is possible to easily evaluate the control to output gain

$$G_{codb}(s) = 20 \log(|K * G(f)|) = 20 \log \left( \left| \frac{V_{in}}{V_{ramp}} * \frac{(s * C_{out} * ESR + 1)}{1 + \frac{s}{w_p * Q} + \left(\frac{s}{w_p}\right)^2} \right| \right)$$

The control to output gain ( $G_{codb}(s)$ ) at 12V is 8dB and when the input is 24V the DC gain is 12dB

$$G_{codb}(0Hz) = 20 \log \left( \left| \frac{V_{in}}{V_{ramp}} \right| \right) \approx 8dB\_to\_12dB$$

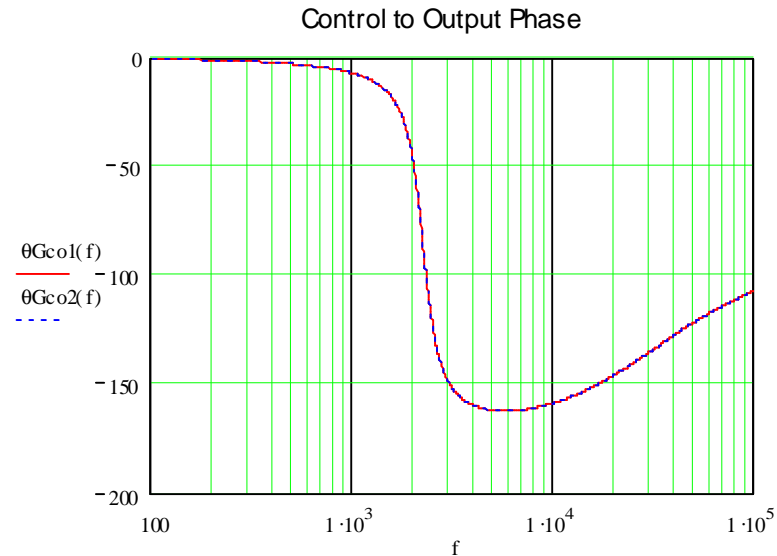
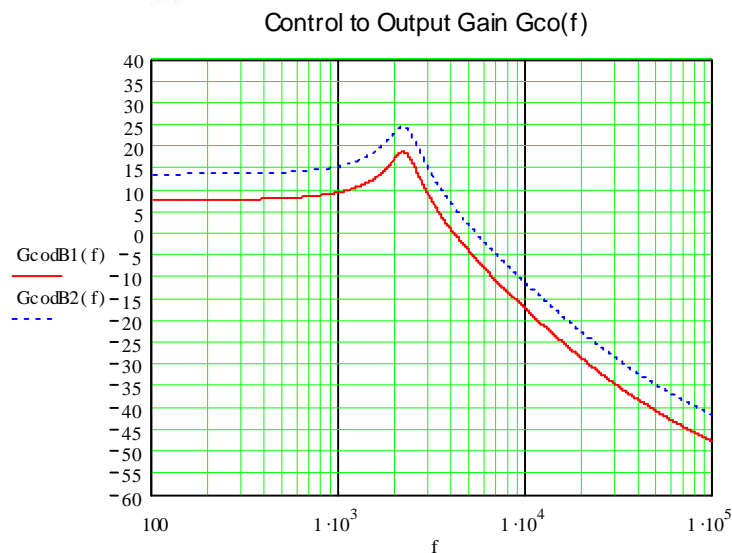
The double pole occurs at roughly 2.25kHz

$$fp1 = fp2 = \frac{1}{2\pi\sqrt{5\mu H * 1000\mu F}} = 2.25kHz$$

The ESR zero occurs at roughly 32 kHz

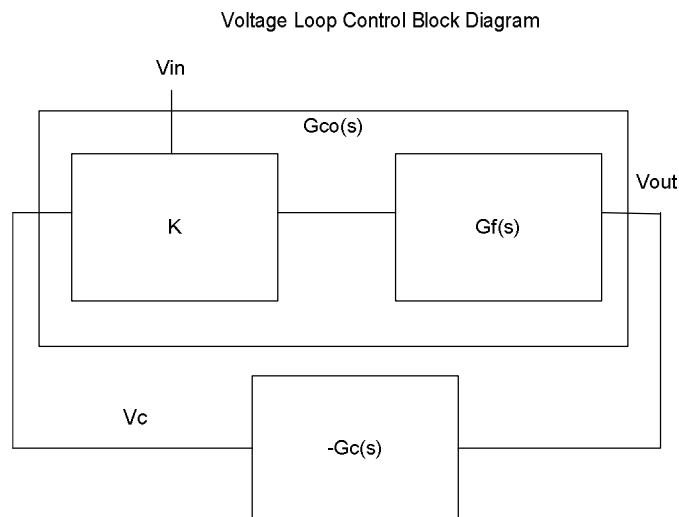
$$f_z = \frac{1}{2\pi(ESR) * C_{out}} \approx 32kHz$$

## The Gco(f) Transfer Function Evaluation



- The Gco(f) of a voltage loop is an LCR network that is unstable by nature
  - The double pole needs to be dampened out by the compensation network Gf(f) for stability.
  - Gco(f) magnitude varies with line which is another challenge for compensating the loop.
  - The voltage loop (T(f)) for this example will be designed for crossover frequency of 10 kHz.
  - The compensation is based on low line input conditions of 12V
    - If you design the loop to crossover at high line, the loop will have less than 45 degrees of phase margin at low line.
  - The first step in compensating the loop is calculating the gain at the desired crossover frequency of 10kHz. The gain at the crossover frequency of 10 kHz is roughly -17dB. The compensation network Gc(f) is going to have to adjust the gain at crossover.

## Closing the Feedback Loop (T(s))



$$T(s) = K * G_f(s) * (-G_c(s))$$

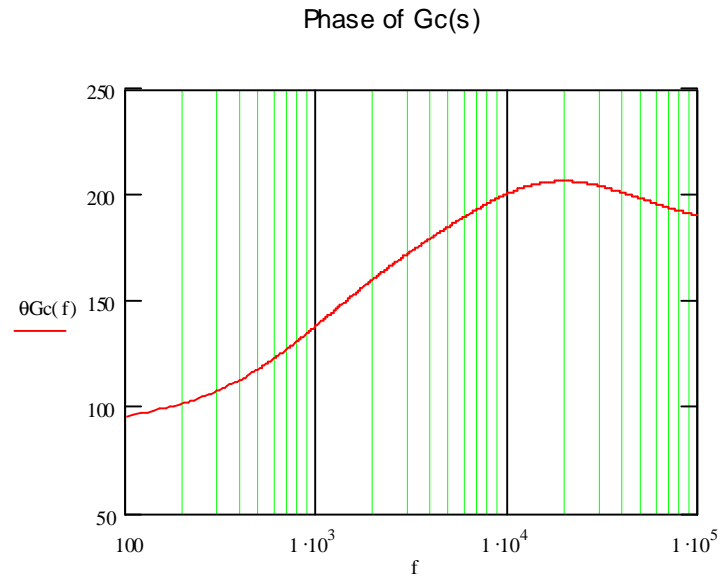
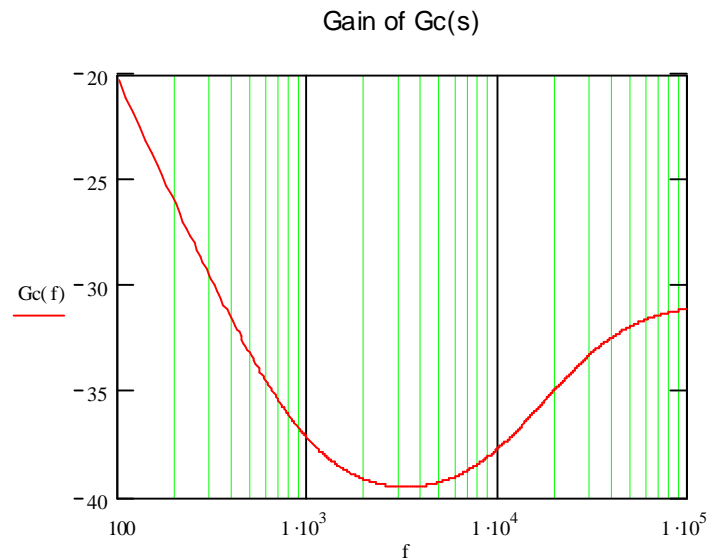
$$G_{co}(s) = K * G_f(s)$$

$$G_{codB}(s) = 20 \log(|K * G_f(s)|)$$

- The first step of compensating the voltage loop was to estimate the  $G_{codB}(s)$  at the desired crossover frequency ( $f_c$ ).
- The next step is to set up the compensation network  $G_c(s)$



# Type 3 Compensation Scheme



- $G_c(s)$  used for this design is type 3 compensator

$$G_c(f) = \frac{\Delta V_c}{\Delta V_{out}} = \frac{(s * R_f * C_z + 1) * (s * R_i * C_{z2} + 1)}{s * R_i * (C_p + C_z) * ((s * R_f * C_p + 1))}$$

## Setting up Type 3 Gc(s) for VM Buck Converter

- The voltage divider  $R_i/(R_i+R)$  sets up the DC output voltage

$$R_i = \frac{R * (V_{out} - V_{ref})}{V_{ref}} = \frac{10k(5V - 2.5V)}{2.5V} = 10K$$

- $R_f$  is sized to offset the  $G_{co}(f)$  gain at the desired crossover frequency

$$R_f = 10^{\frac{-G_{codB}(f_c)}{20}} \approx 37k$$

$$R_f = 36k$$

Choose a standard resistor for  $R_f$  of 36k ohm

## Setting up Type 3 Gc(s) for VM Buck Converter

- The first zero of the compensation scheme will be set to give the feedback loop a phase boost at 1/10 of the double pole frequency of the output filter (Gf(s))

$$R_i = 10k$$

$$R_f = 36k$$

$$f_{z1} = \frac{1}{2\pi\sqrt{L_{out} * C_{out}}} = 2.25kHz \quad C_z = \frac{1}{2\pi * f_{z1} * R_f} \approx 22nF$$

- The second zero of the compensation scheme will be set to add 45 degrees of PM at crossover

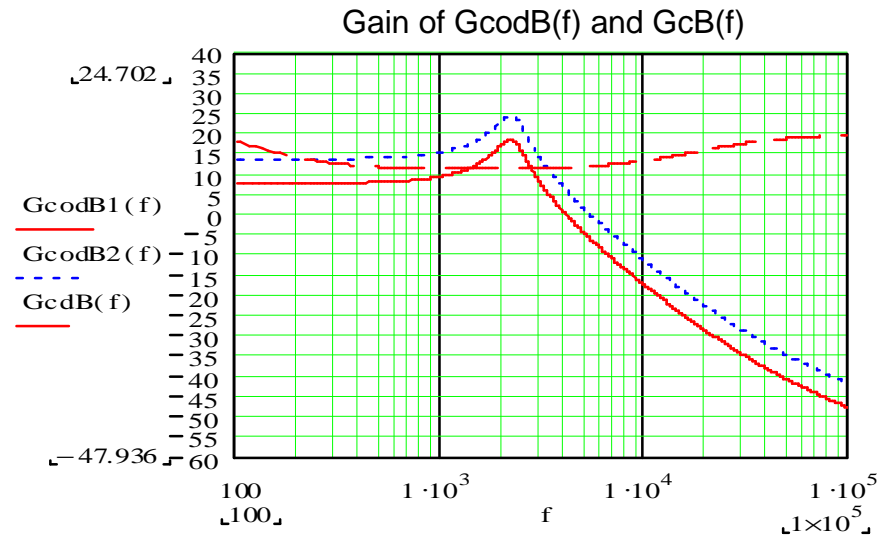
$$f_{z2} = \frac{f_s}{10} = 10kHz \quad C_{z2} = \frac{1}{2\pi * R_i * f_{z2}} \approx 1.5nF$$

- A pole will be added to cancel out the effects of the ESR zero

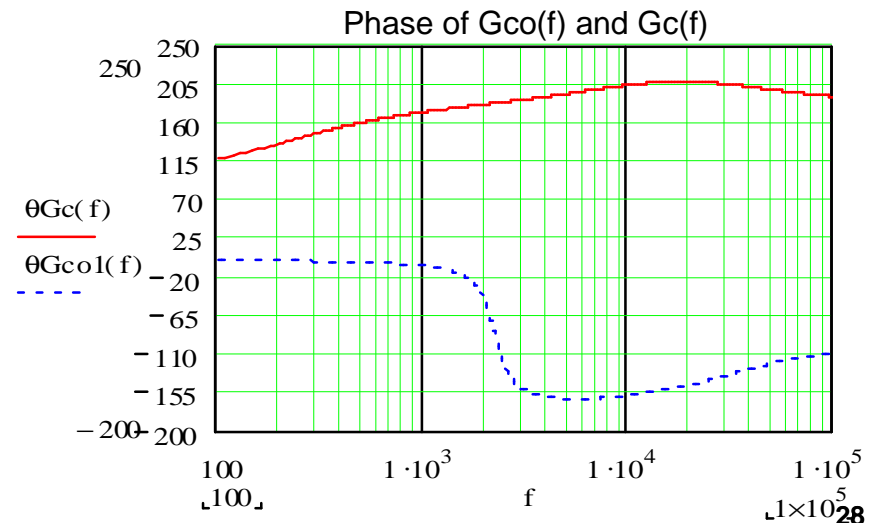
$$f_p = \frac{1}{2\pi * C_{out} * ESR} \approx 32kHz \quad C_p = \frac{1}{2\pi * R_f * f_p} \approx 150pF$$

## Bode Plots of The VM Buck Converter Design Example

- The sum of  $G_{codB}(f)$  and  $G_{cB}(f)$  is the loop gain  $T_{db}(s)$

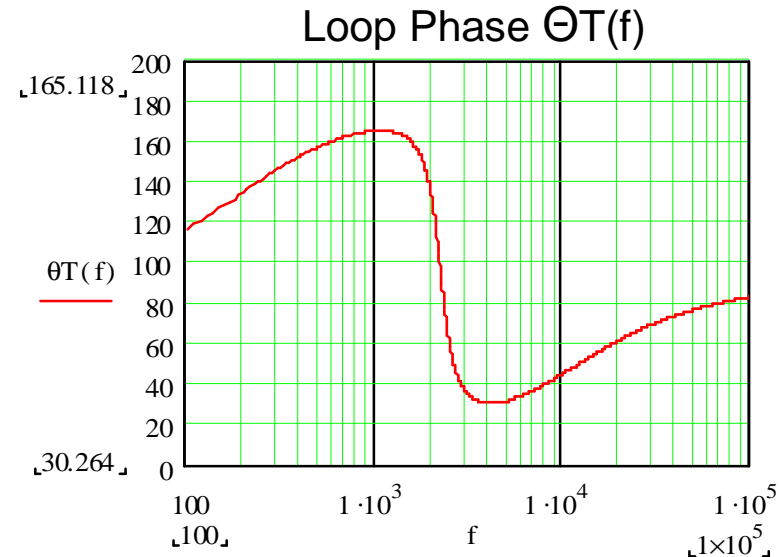
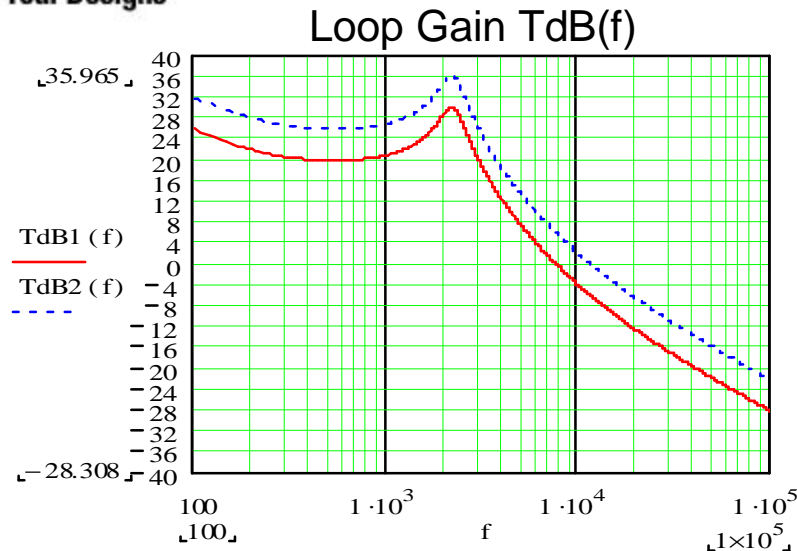


- The sum of  $\Theta G_{co}(f)$  and  $\Theta G_c(f)$  is the loop phase  $\Theta T(s)$



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## Evaluate The Voltage Feedback Loop $T(f)$



From the bode plots of loop gain it can be observed that voltage loop  $T(s)$  was stable

- The loop crossover was between 8 and 15 kHz

- The PM was between 40 and 45 degrees

- \*The design goal was to have better than 45 degrees of phase margin but the system can work with as little as 35 degrees of phase margin

- \*The real point of instability occurs when the loop is 180 degrees out of phase.

- >This is because the loop will have positive feedback

- >When the loop is 180 degrees out of phase the gain needs to be less than -6dB to ensure stability

## Voltage Feed Forward Makes Compensation for VM Control Easier

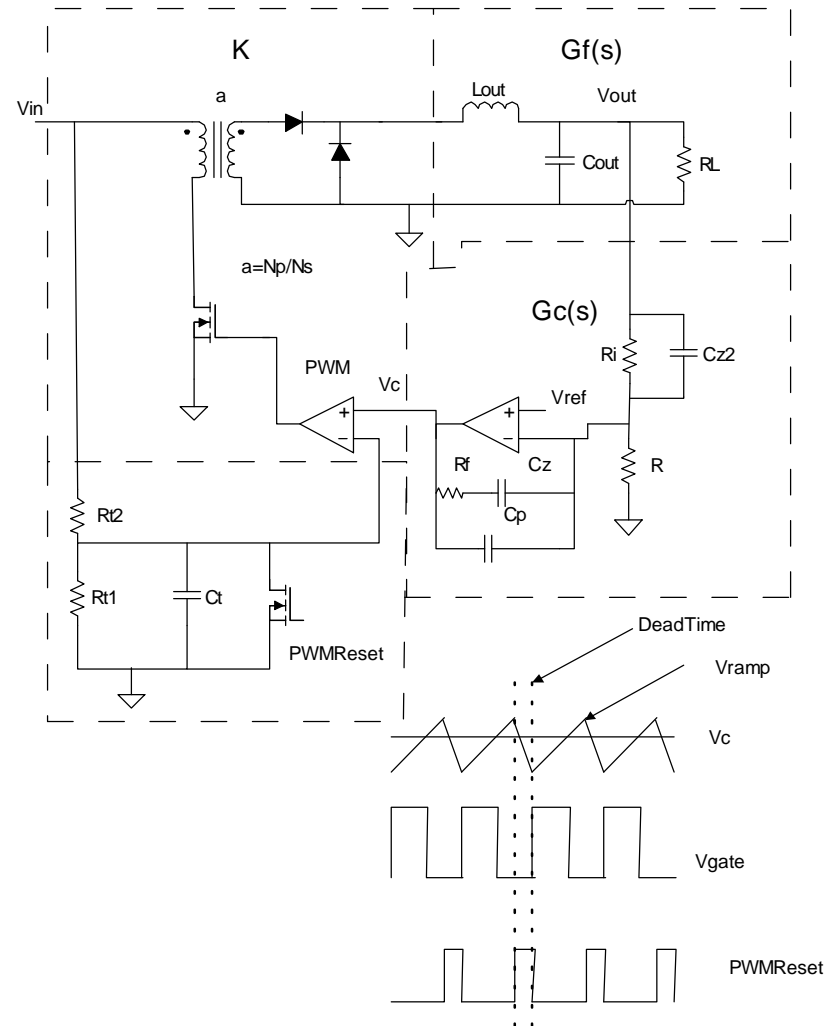
### •Voltage Feed Forward (VFF)

- Keeps the  $G_{co}(s)$  gain constant with varying input voltage.
- This is generally accomplished by making the PWM ramp voltage proportional to the input voltage
- Compensating a buck derived converter with VFF is much easier.

$$V_{ramp} = V_{FF} = \frac{V_{in} * R_{t1}}{R_{t1} + R_{t2}}$$

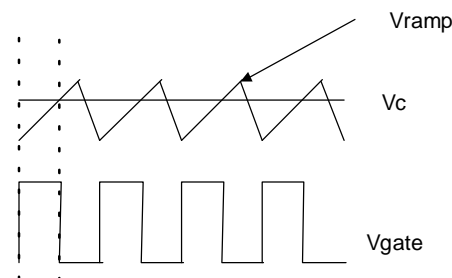
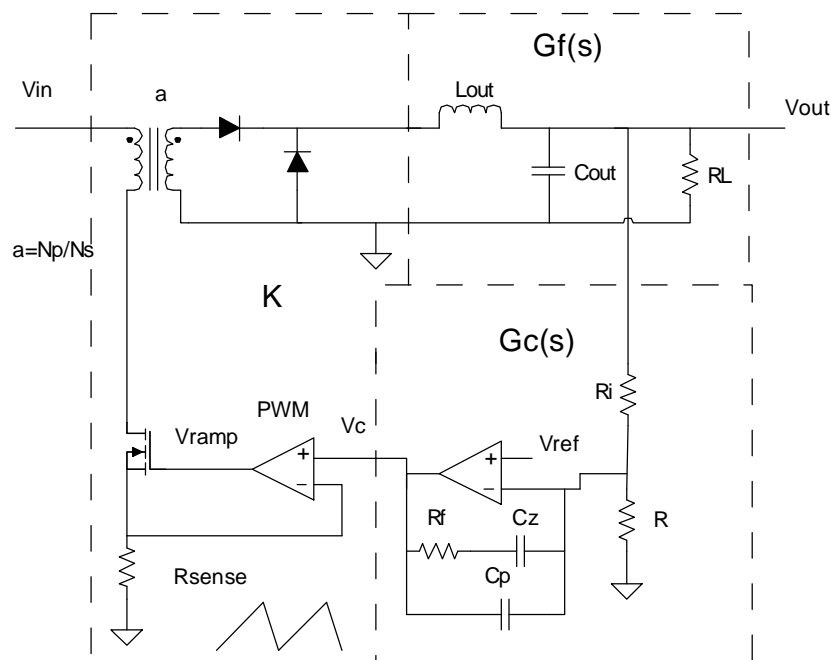
$$K = \frac{1}{a} \frac{V_{in}}{V_{ramp}} = \frac{1}{a} \frac{V_{in}}{\frac{V_{in} * R_{t1}}{R_{t1} + R_{t2}}} = \frac{1}{a} \frac{R_{t1} + R_{t2}}{R_{t1}}$$

$$G_{co}(s) = 20 \log \left( \frac{1}{a} \frac{R_{t1} + R_{t2}}{R_{t1}} * \frac{(s * C_{out} * ESR + 1)}{1 + \frac{s}{w_p * Q} + \left( \frac{s}{w_p} \right)^2} \right)$$



# Peak Current Mode (PCM) Control

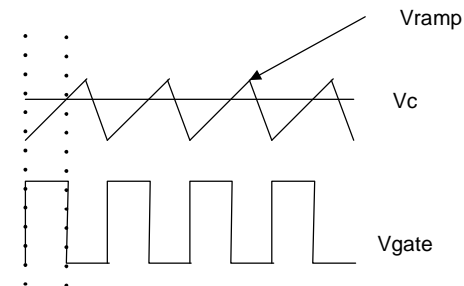
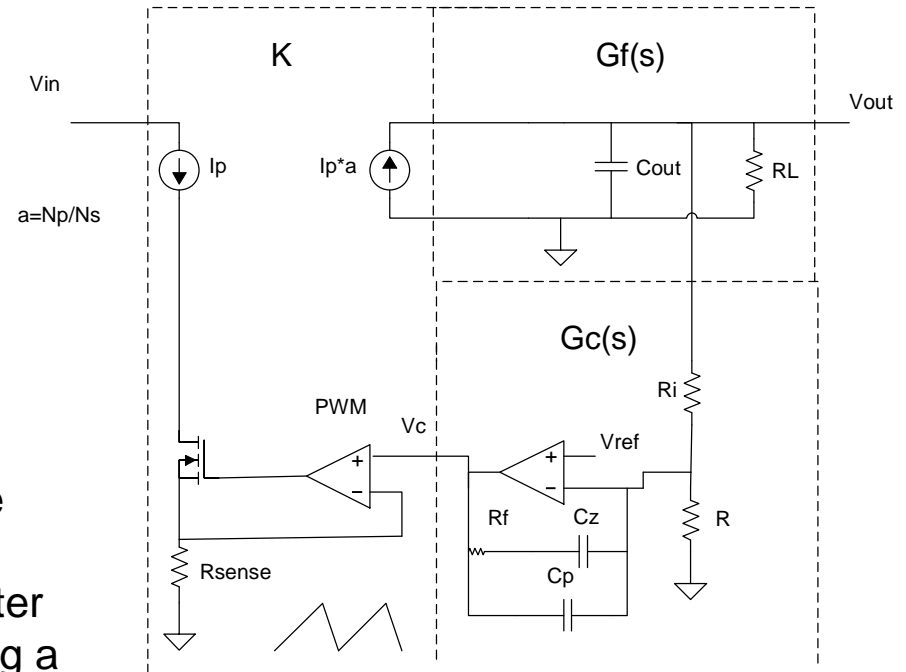
- A Peak Current Mode Control forward converter
  - PWM Ramp is built of a current sense signal



# Peak Current Mode (PCM) Control

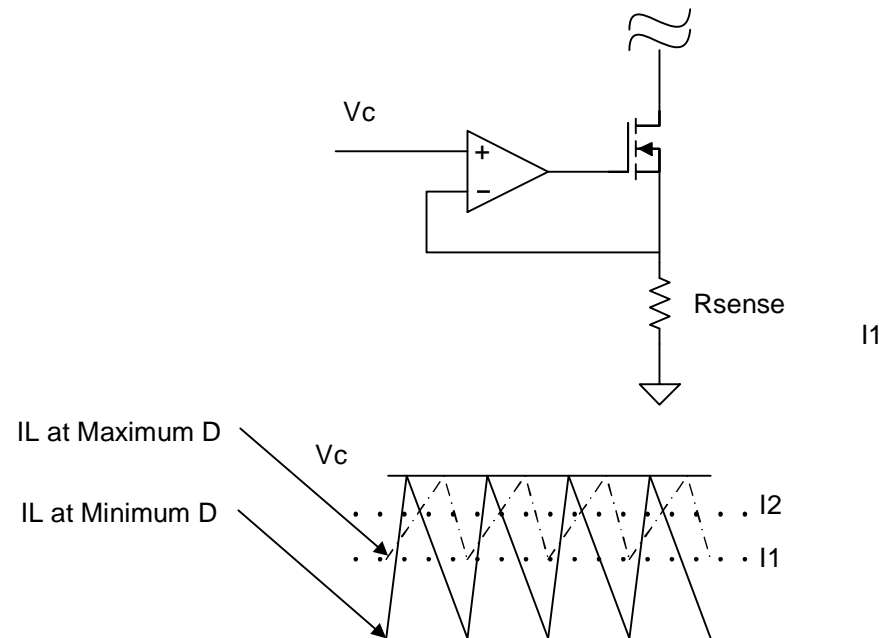
The output inductor in the output filter ( $G_f(s)$ ) look like a current source

- This removes the double pole from the voltage loop  $T(s)$  and makes the loop much easier to compensate
- To compensate PCM control converter the designer can get away with using a type 2 compensation scheme  $G_c(s)$



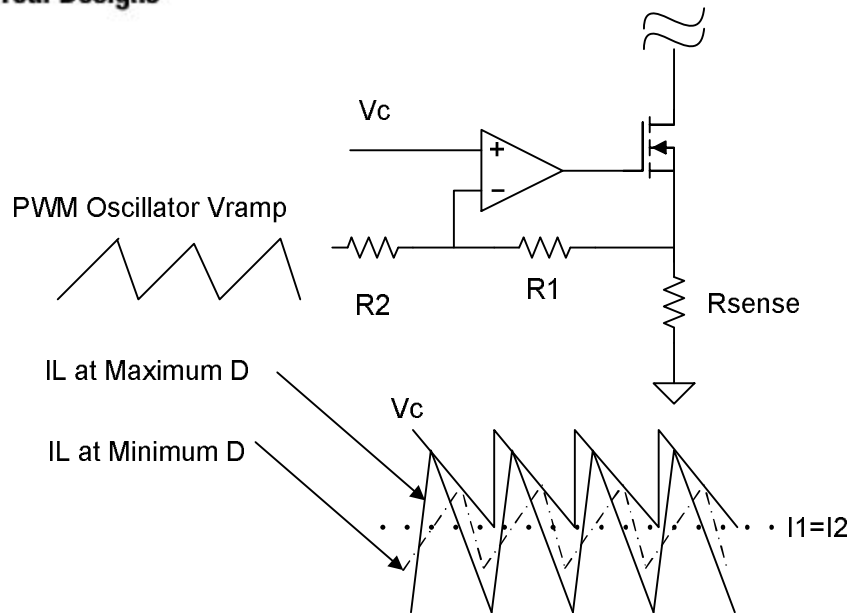


## Known Instability in Peak Current Mode Control Power Converters



- The objective in peak current mode control is to control the inductors average current
  - At max. input voltage the converter is at minimum D and the inductors ripple current is at its maximum
  - At min. input voltage the converter is at maximum D and the inductors ripple current is at its minimum
  - Any change in duty cycle can cause a change in average inductor current creating an instability.
  - This problem can be corrected with slope compensation

# Slope Compensation

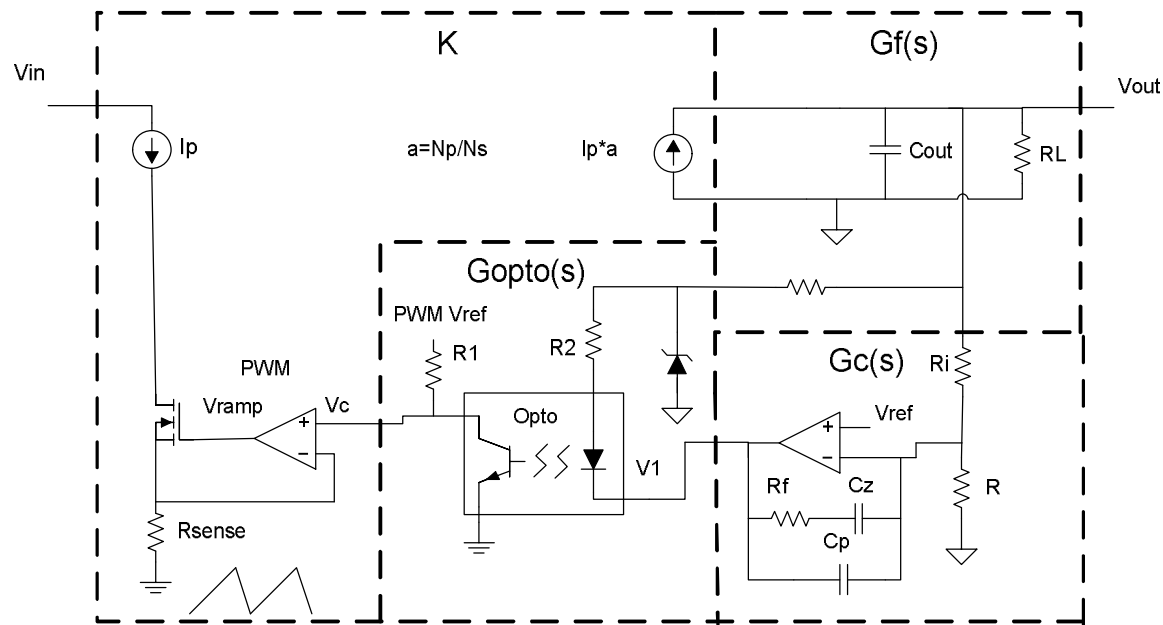


$$V_{slope} \geq \frac{V_{out} * (1 - D_{min}) * R_{sense}}{2 * L_{out} * f_s}$$

$$R_2 = \frac{R_1 * V_{osc}}{V_{slope}}$$

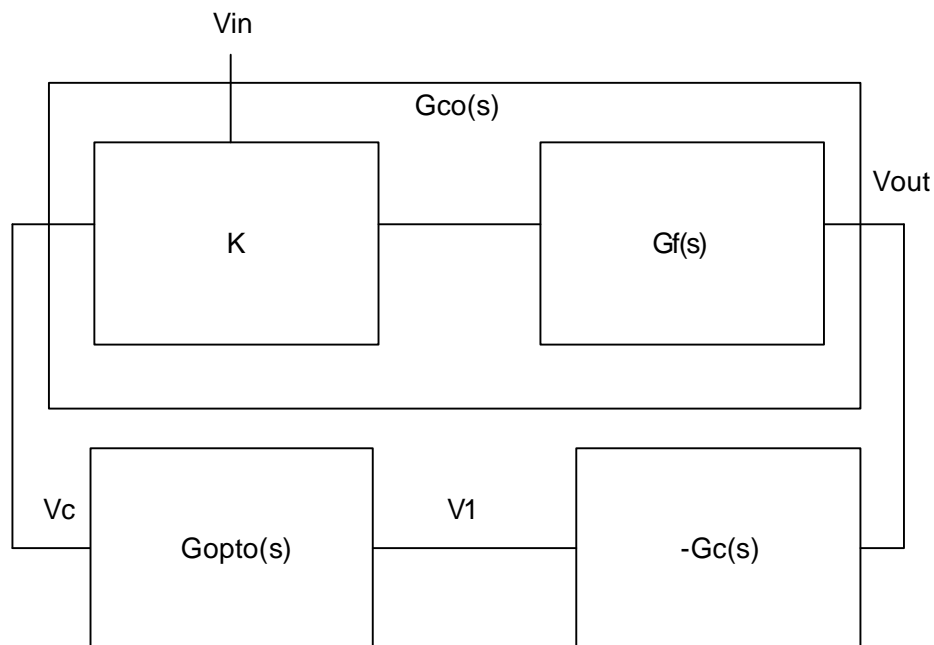
- By adding a ramp to the current sense signal the converter will be forced to have the same average inductor current independent of duty cycle
  - The slope can be added by summing in a portion of the PWM's oscillator ramp into the current sense signal.
  - To ensure stability the added slope needs to be at least equal to  $\frac{1}{2}$  the inductor currents down slope

## Example of a Peak Current Mode Forward Converter



- Type 2 compensation is generally used for feedback  $G_c(s)$  in peak current mode converters
- A transformer and opto isolator are generally used to isolate the input from the output
  - The opto isolator will add a gain block  $G_{opto}(s)$  that will have an affect on the loop compensation

## Control Block Diagram For PCM Forward Converter Design Example



## Review Control to Output $G_{co}(f)$ Transfer Block for This Design Example

$$a = \frac{N_p}{N_s} = \frac{V_p}{V_s} = \frac{I_s}{I_p}$$

$$K = \frac{\Delta I_{out}}{\Delta I_{in}} = \frac{\Delta V_{out}}{\Delta V_c} = \frac{a * R_L}{R_{sense}}$$

$$G_f(s) = \frac{(s * C_{out} * ESR + 1)}{(s * R_L * C_{out} + 1)}$$

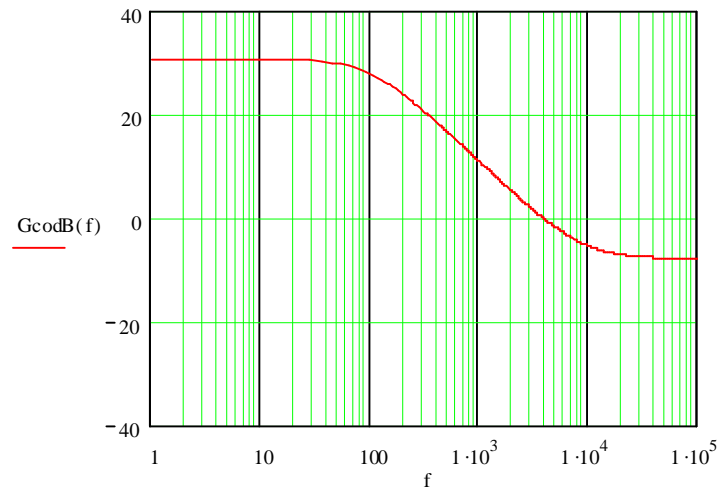
$$G_{co}(s) = \frac{\Delta V_{out}}{\Delta V_c} = \frac{a * R_L}{R_{sense}} \frac{(s * C_{out} * ESR + 1)}{(s * R_L * C_{out} + 1)}$$

$$G_{coB}(s) = 20 \log \left( \frac{a * R_L}{R_{sense}} \frac{(s * C_{out} * ESR + 1)}{(s * R_L * C_{out} + 1)} \right)$$

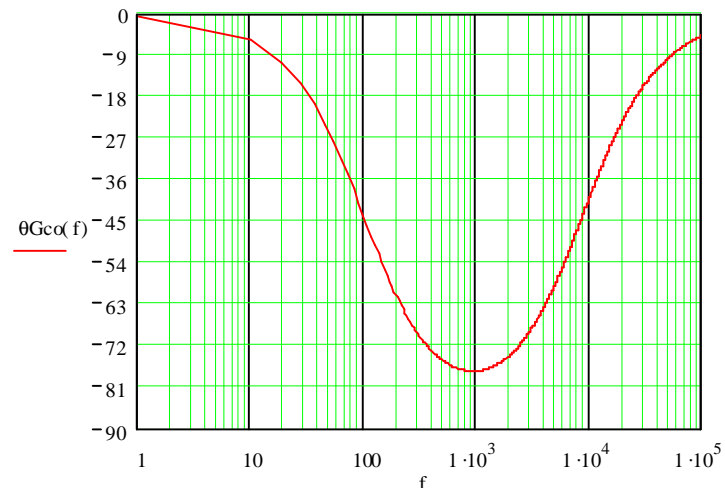
Peak current mode control makes the filter inductor ( $L_{out}$ ) look like a current source.

- The  $G_{co}(s)$  becomes a first order filter which is much easier to compensate

Power Stage Gain



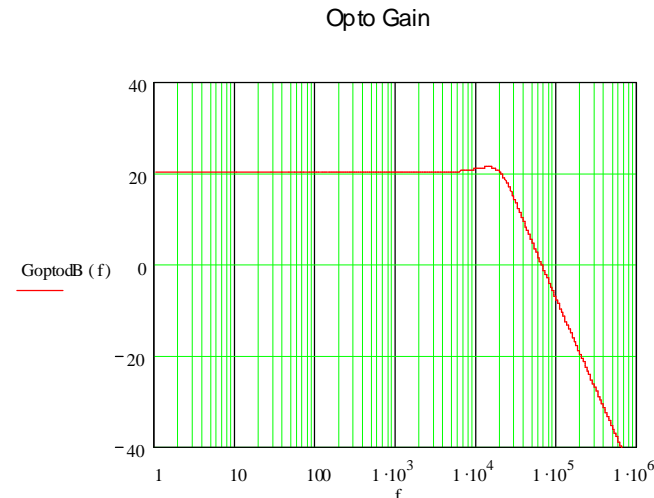
Power Stage Phase



# Gopto(f) Gain Block

- The opto isolator small signal characteristics (Gopto(f)) are over looked in many designs and can cause problems
  - The opto isolator small signal response looks similar to an LCR filter
  - When designing a control loop the opto isolator will limit the amount of bandwidth that can be designed in the voltage loop T(s).
    - The voltage loop needs to be designed to crossover before the double pole of the opto isolator
    - These specifications are not generally given in the opto data sheet and should be measured

$$G_{opto}(f) = \frac{\Delta V_c}{\Delta V_I} = \frac{\frac{R_1}{R_2}}{1 + \frac{s}{2j\pi f_p * Q} + \left(\frac{s}{2j\pi f_p}\right)^2}$$



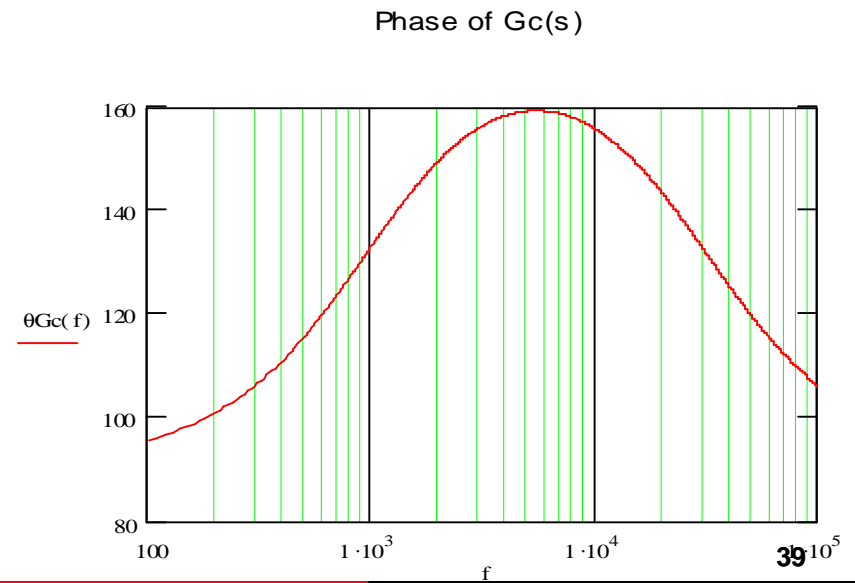
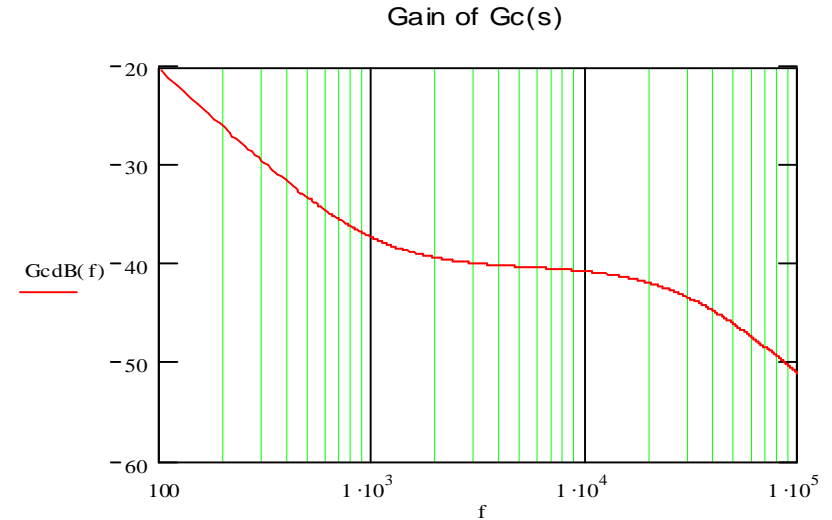
Date 2004

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# Type 2 Compensation Feed Back (Gc(s))

A type 2 compensation is generally used to compensate peak mode control converters  
- A type 3 amplifier can also be used

$$G_c(f) = \frac{\Delta V_1}{\Delta V_{out}} = \frac{(s * R_f * C_z + 1)}{s * R_i * (C_p + C_z)((s * R_f * C_p + 1))}$$



Date 2004

REAL WORLD SIGNAL PROCESSING™

TEXAS INSTRUMENTS

- Power Converter Specifications
  - $V_{in} = 300 \text{ to } 400\text{V}$
  - $V_{out} = 24 \text{ V}$
  - $V_{ref} = 2.5 \text{ V}$
  - Switching Frequency  $f_s = 200 \text{ kHz}$
  - $L_{out} = 50 \text{ uH}$
  - $C_{out} = 270 \text{ uF}$  with an ESR of 68 mohm
  - $P_{out} = 100\text{W}$  / Load Impedance ( $R_L$ ) = 5.76ohm
  - PWM Ramp ( $V_{ramp}$ ) = 5V
  - $a = N_p/N_s = 5.98$
  - Opto pole  $f_p = 20 \text{ kHz}$
  - Loop crossover  $f_c = 10 \text{ kHz} < f_p < f_s/6$



# Gopto(s)

- The opto used for this design was an HP11A with the following characteristics
  - Resistors R1 and R2 were selected to give the opto a DC gain of 20dB

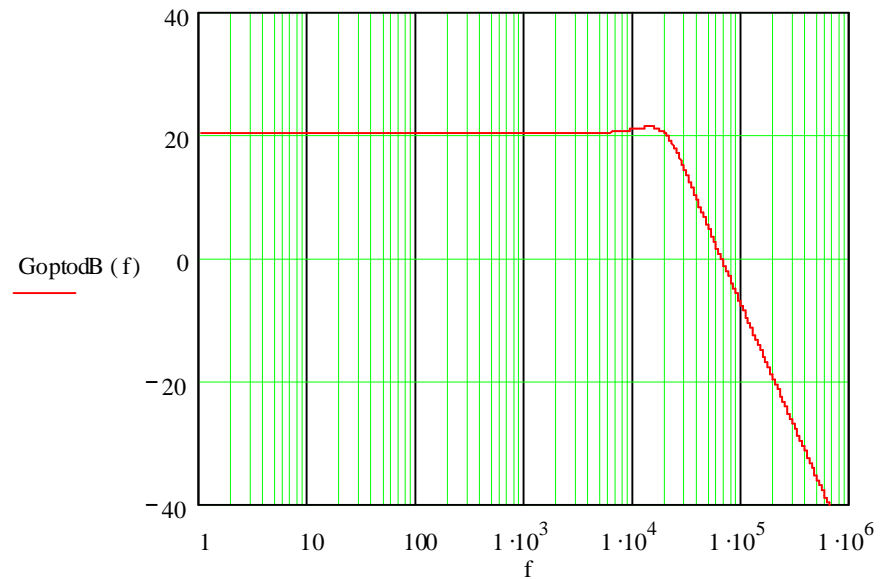
R1 = 1kohm

R2 = 100ohm

fp = 20kHz

$$G_{opto}(f) = \frac{\Delta V_c}{\Delta V_1} = \frac{\frac{R_1}{R_2}}{1 + \frac{s}{2j\pi f p * Q} + \left(\frac{s}{2j\pi f p}\right)^2}$$

Opto Gain



## Setting up Type 2 Gc(s) for PCM Forward Converter in CCM Mode

- The voltage divider  $R_i/(R_i+R)$  sets up the DC output voltage

$$R_i = \frac{R * (V_{out} - V_{ref})}{V_{ref}} = \frac{1k(24V - 2.5V)}{2.5V} \approx 8.66K$$

- $R_f$  is sized to adjust the loop crossover at 0dB
  - This should be calculated base on maximum load conditions when  $R_L$  is at its smallest value to ensure loop stability

$$R_f = 10^{\frac{-G_{codB}(f_c) - G_{opto}(f_c)}{20}} \approx 1.45k$$

$$R_f = 1.43k$$

Choose a standard resistor for  $R_f$  of 1.43k ohm

## Setting up Type 2 Gc(s) for VM Buck Converter

- A zero of the compensation scheme will be set to give the feedback loop a phase boost at the crossover frequency

$$f_z = f_c = 10\text{kHz} \qquad C_z = \frac{1}{2\pi f_c * R_f} \approx 10\text{nF}$$

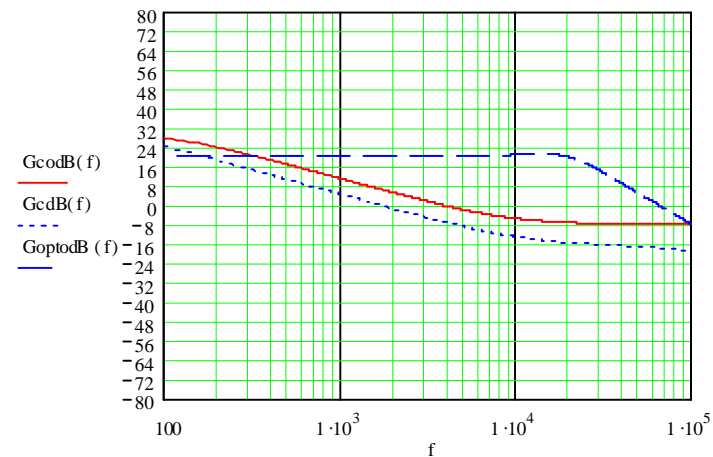
- A pole will be added to roll off the gain at one half the switching frequency

$$C_p = \frac{1}{2\pi \frac{f_s}{2} R_f} = 1\text{nF}$$

## Bode Plots of The VM Buck Converter Design Example

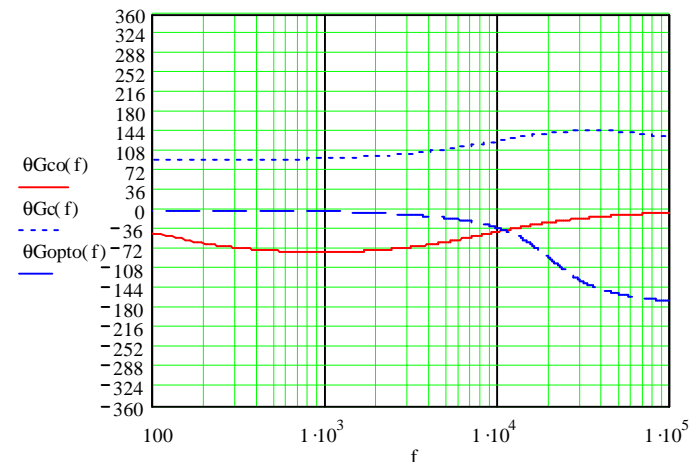
- The sum of  $G_{codB}(f)$ ,  $G_{cdB}(f)$  and  $G_{optodB}(f)$  is the loop gain  $T_{db}(s)$

Gain of  $G_{codB}(f)$ ,  $G_{cdB}(f)$  and  $G_{optodB}(f)$



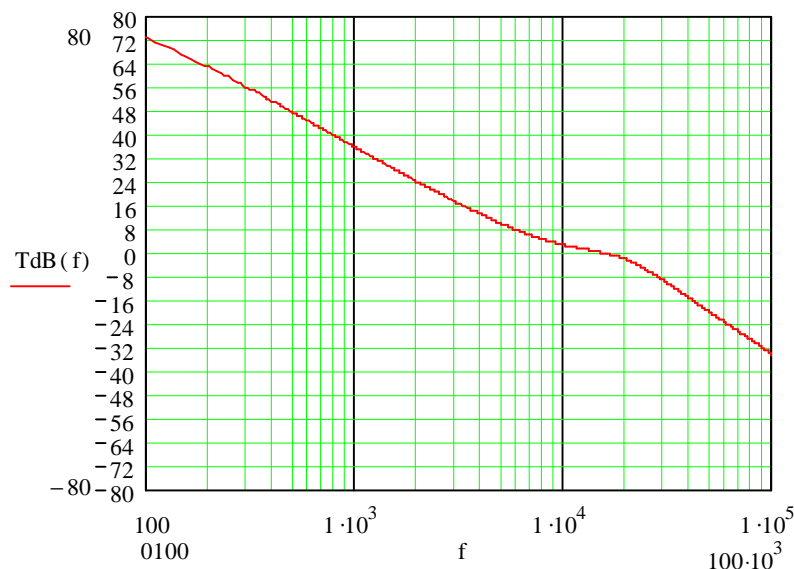
- The sum of  $\Theta_{Gco}(f)$ ,  $\Theta_{Gc}(f)$  and  $\Theta_{Gopto}(f)$  is the loop phase  $\Theta_T(f)$

Phase of  $G_{co}(f)$ ,  $G_c(f)$  and  $G_{opto}(f)$

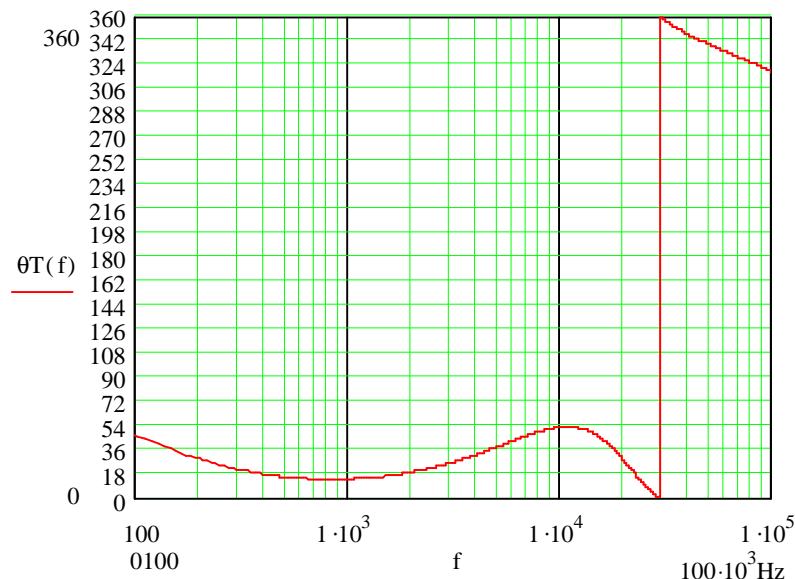


## Evaluate The Voltage Feedback Loop $T(f)$ Peak Current Mode Forward Converter

Loop Gain  $TdB(f)$



Loop Phase  $\theta T(f)$



From the bode plots of the voltage loop ( $T(f)$ ) it can be observed that the loop was stable

- The loop crossover occurred at roughly 15kHz
- The PM was roughly 54 degrees at crossover

# Recap

- Bode plots can be used to compensate feedback loops
- Most power supply control loops can be compensated using a type 2 or type 3 compensation scheme
- Voltage Mode Control
  - is mostly used with buck converters
  - can be done with flyback and Boost converters but is very complicated and should be avoided
  - is typically compensated with a type 3 compensation scheme ( $G_c(f)$ )
- Current Mode Control
  - simplifies the transfer function by making the filter inductor look like a current source
  - is typically compensated with a type 2 compensation network ( $G_c(f)$ )
- Opto Isolators have a small signal response that affects loop gain

## Gco(f) Transfer Functions of Different Type of Power Converters

Voltage Mode Control, Buck/Forward, Continuous Conduction Mode (CCM)

$$G_{co}(s) = \frac{1}{a} \frac{V_{in}}{V_{ramp}} \frac{(s * ESR * C_{out} + 1)}{1 + \frac{s}{w_p * Q} + \left( \frac{s}{w_p} \right)^2}$$

Voltage Mode Control, Buck/Forward, Continuous Conduction Mode (CCM) with VFF

$$G_{co}(s) = \frac{1}{a} \frac{R_{1t} + R_{2t}}{R_{1t}} \frac{(s * ESR * C_{out} + 1)}{1 + \frac{s}{w_p * Q} + \left( \frac{s}{w_p} \right)^2}$$

Current Mode Control, Buck/Forward, Continuous Conduction Mode (CCM)

$$G_{co}(s) = \frac{a * R_L}{R_{sense}} \frac{(s * C_{out} * ESR + 1)}{(s * R_L * C_{out} + 1)}$$

## Gco(f) Transfer Functions of Different Type of Power Converters

Voltage Mode Control, Flyback, Continuous Conduction Mode (CCM)

\*This technique is complex and easier methods are available

$$w_p = \frac{1-D}{\sqrt{LC}}$$

$$G_{co}(s) = \frac{1}{a} \frac{V_{in}}{V_{ramp}} \left( 1 + \frac{V_{out}}{V_{in}} \right)^2 \frac{(s * ESR * C_{out} + 1)}{1 + \frac{s}{w_p * Q} + \left( \frac{s}{w_p} \right)^2} \left( 1 - \frac{s * L}{RL} \frac{V_{out}(V_{out} + V_{in})}{V_{in}} \right)$$

Current Mode Mode Control, Flyback, Continuous Conduction Mode (CCM)

\*Need to roll off the gain extremely early due to the RHP Zero

$$G_{co}(s) = a \frac{R_o}{R_{sense}} \frac{V_{in}}{(V_{out} + V_{in})} \frac{(s * ESR * C_{out} + 1)}{(s * RL * C_{out} + 1)} \left( 1 - \frac{s * L}{RL} \frac{V_{out}(V_{out} + V_{in})}{V_{in}} \right)$$

Current Mode Control, Flyback, Discontinuous Conduction Mode (DCM)

\*The right half plane zero is not present in DCM flyback converters

\*This technique is more popular and easier to compensate

$$G_{co}(s) = a \frac{R_o}{R_{sense}} \frac{V_{in}}{(V_{out} + V_{in})} \frac{(s * ESR * C_{out} + 1)}{(s * RL * C_{out} + 1)}$$



## Gco(f) Transfer Functions of Different Type of Power Converters

Voltage Mode Control, Boost, Continuous Conduction Mode (CCM)

\*This technique is complex and easier methods are available

$$w_p = \frac{1-D}{\sqrt{LC}}$$

$$G_{co}(s) = \frac{1}{a} \frac{V_{in}}{V_{ramp}} \left( \frac{V_{out}}{V_{in}} \right)^2 \frac{(s * ESR * C_{out} + 1)}{1 + \frac{s}{w_p * Q} + \left( \frac{s}{w_p} \right)^2} \left( 1 - \frac{s * L}{RL} \left( \frac{V_{out}}{V_{in}} \right)^2 \right)$$

Current Mode Control, Boost, Continuous Conduction Mode (CCM)

\*Need to roll off the gain extremely early due to the RHP Zero

$$G_{co}(s) = a \frac{R_o}{R_{sense}} \frac{V_{in}}{V_{out}} \frac{(s * ESR * C_{out} + 1)}{(s * RL * C_{out} + 1)} \left( 1 - \frac{s * L}{RL} \left( \frac{V_{out}}{V_{in}} \right)^2 \right)$$

Current Mode Control, Boost, Discontinuous Conduction Mode (DCM)

\*The right half plane zero is not present in DCM flyback converters

\*This technique is more popular

$$G_{co}(s) = a \frac{R_o}{R_{sense}} \frac{V_{in}}{V_{out}} \frac{(s * ESR * C_{out} + 1)}{(s * RL * C_{out} + 1)}$$

# Home Work

- Use bode plot to compensate a forward converter with the following specifications
- Power Converter Specifications
  - $V_{in} = 300 \text{ to } 400\text{V}$
  - $V_{out} = 24\text{V}$
  - $V_{ref} = 2.5\text{V}$
  - Switching Frequency  $f_s = 200 \text{ kHz}$
  - $L_{out} = 50 \text{ uH}$
  - $C_{out} = 270 \text{ uF}$  with an ESR of 68 mohm
  - $P_{out} = 100\text{W}$  / Load Impedance ( $R_L$ ) = 5.76ohm
  - PWM Ramp ( $V_{ramp}$ ) = 5V
  - $a = N_p/N_s = 5.98$
  - Opto pole  $f_p = 20 \text{ kHz}$ ,  $R_1 = 1\text{K}$ ,  $R_2 = 100 \text{ ohm}$
  - Loop crossover  $f_c = 10 \text{ kHz} < f_p < f_s/6$

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