

KA7500C

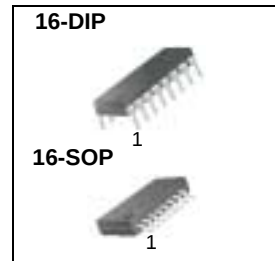
SMPS Controller

Features

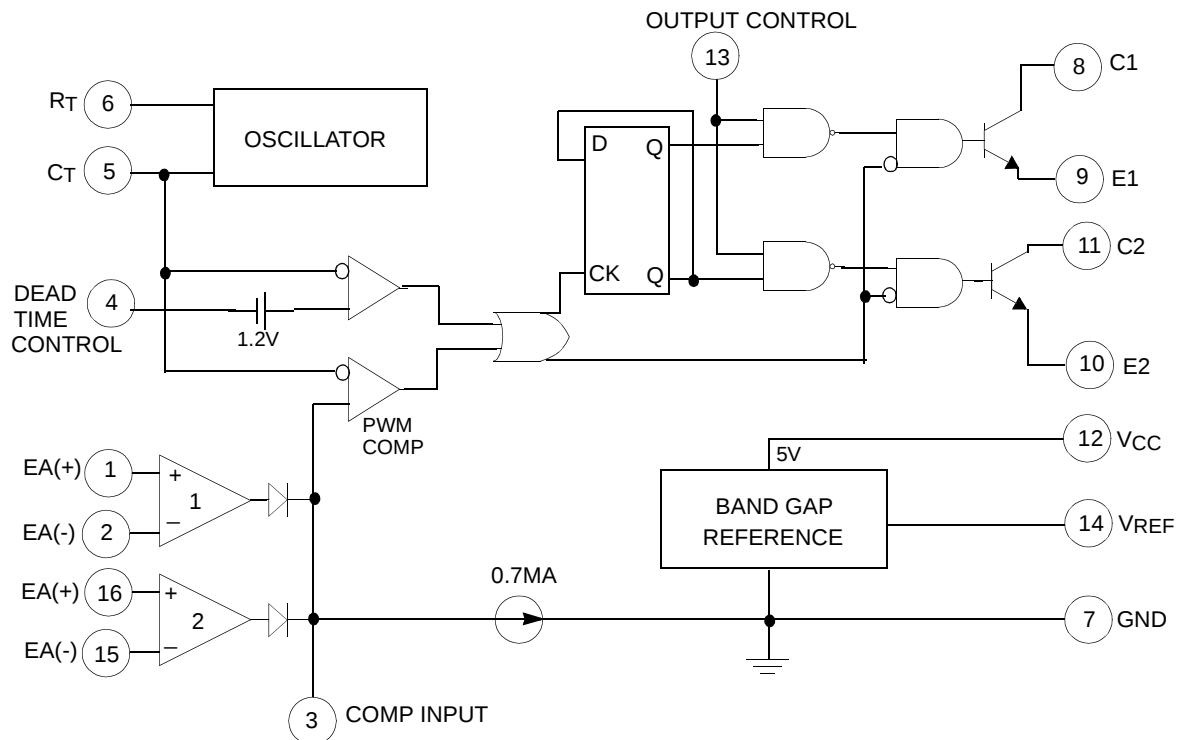
- Internal Regulator Provides a Stable 5V Reference Supply Trimmed to $\pm 1\%$ Accuracy.
- Uncommitted Output TR for 200mA Sink or Source Current
- Output Control for Push-Pull or Single-Ended Operation
- Variable Duty Cycle by Dead Time Control (Pin 4) Complete PWM Control Circuit
- On-Chip Oscillator With Master or Slave Operation
- Internal Circuit Prohibits Double Pulse at Either Output

Description

The KA7500C is used for the control circuit of the pulse width modulation switching regulator. The KA7500C consists of 5V reference voltage circuit, two error amplifiers, flip flop, an output control circuit, a PWM comparator, a dead time comparator and an oscillator. This device can be operated in the switching frequency of 1kHz to 300kHz. The precision of voltage reference (V_{ref}) is improved up to $\pm 1\%$ with trimming. This provides a better output voltage regulation. The operating temperature range is $-25^{\circ}\text{C} \sim +85^{\circ}\text{C}$.



Internal Block Diagram



Rev. 1.0.1

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	V _{CC}	42	V
Collector Supply Voltage	V _C	42	V
Output Current	I _O	250	mA
Amplifier Input Voltage	V _{IN}	V _{CC} +0.3	V
Power Dissipation (T _A = 25°C)	P _D	1 (KA7500C) 0.9 (KA7500CD)	W
Operating Temperature Range	T _{OPR}	-25 ~ +85	°C
Storage Temperature Range	T _{STG}	-65 ~ +150	°C
Junction Temperature	T _j	125	°C

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power Supply Voltage	V _{CC}	7.0	15	40	V
Collector Output Voltage	V _{C1} , V _{C2}	-	30	40	V
Collector Output Current (Each Transistor)	I _{C1} , I _{C2}	-	-	200	mA
Amplifier Input Voltage	V _{IN}	0.3	-	V _{CC} -2.0	V
Current Into Feedback Terminal	I _{fb}	-	-	0.3	mA
Reference Output Current	I _{ref}	-	-	10	mA
Timing Resistor	R _T	1.8	30	500	KΩ
Timing Capacitor	C _T	0.0047	0.001	10	μF
Oscillator Frequency	f _{osc}	1.0	40	200	kHz
PWM Input Voltage (Pins3, 4, 13)	-	0.3	-	5.3	V

Electrical Characteristics

($V_{CC} = 20V$, $f = 10kHz$, $T_A = -25^{\circ}C$ to $+85^{\circ}C$, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
REFERENCE SECTION						
Reference Output Voltage	VREF	IREF = 1mA, TA=25°C(Note1)	4.95	5.0	5.05	V
		IREF = 1mA	4.9	5.0	5.1	
Line Regulation	RLine	VCC = 7V to 40V	-	2.0	25	mV
Load Regulation	RLOAD	IREF = 1mA to 10mA	-	1.0	15	mV
Short Circuit Output Current	ISC	VREF = 0V	10	35	50	mA
OSCILLATOR SECTION						
Oscillation Frequency	fosc	CT = 0.001μF, RT = 30KΩ	-	40	-	kHz
		CT = 0.001μF, RT = 12KΩ,TA=25°C	9.2	10	10.8	
		CT = 0.001μF, RT = 30KΩ, TA=Tlow to Thigh	9.0	-	12	
Frequency Change with Temperature	Δf/ΔT	CT = 0.01μF, RT = 12KΩ	-	-	2	%
DEAD TIME CONTROL SECTION						
Input Bias Current	IBIAS	VCC = 15V, 0V ≤ V4 ≤ 5.25V	-	-2.0	-10	μA
Maximum Duty Cycle	D(MAX)	VCC = 15V, V4 = 0V O.C Pin = VREF	45	-	-	%
Input Threshold Voltage	VITH	Zero Duty Cycle	-	3.0	3.3	V
		Max. Duty Cycle	0	-	-	
ERROR AMP SECTION						
Input Offset Voltage	VIO	V3 = 2.5V	-	2.0	10	mV
Input Offset Current	IIO	V3 = 2.5V	-	25	250	mA
Input Bias Current	IBIAS	V3 = 2.5V	-	0.2	1.0	μA
Common Mode Input Voltage	VCM	7V ≤ VCC ≤ 40V	-0.3	-	VCC	V
Open-Loop Voltage Gain	GVO	0.5V ≤ V3 ≤ 3.5V	70	95	-	dB
Unit-Gain Bandwidth	BW	-	-	650	-	kHz
PWM COMPARATOR SECTION						
Input Threshold Voltage	VITH	Zero Duty Cycle	-	4	4.5	V
Input Sink Current	ISINK	V3 = 0.7V	-0.3	-0.7	-	mA
OUTPUT SECTION						
Output Saturation Voltage Common Emitter	VCE(SAT)	VE = 0V, IC = 200mA	-	1.1	1.3	V

Electrical Characteristics (Continued)(V_{CC} = 20V, f = 10kHz, T_A = -25°C to +85°C, unless otherwise specified)

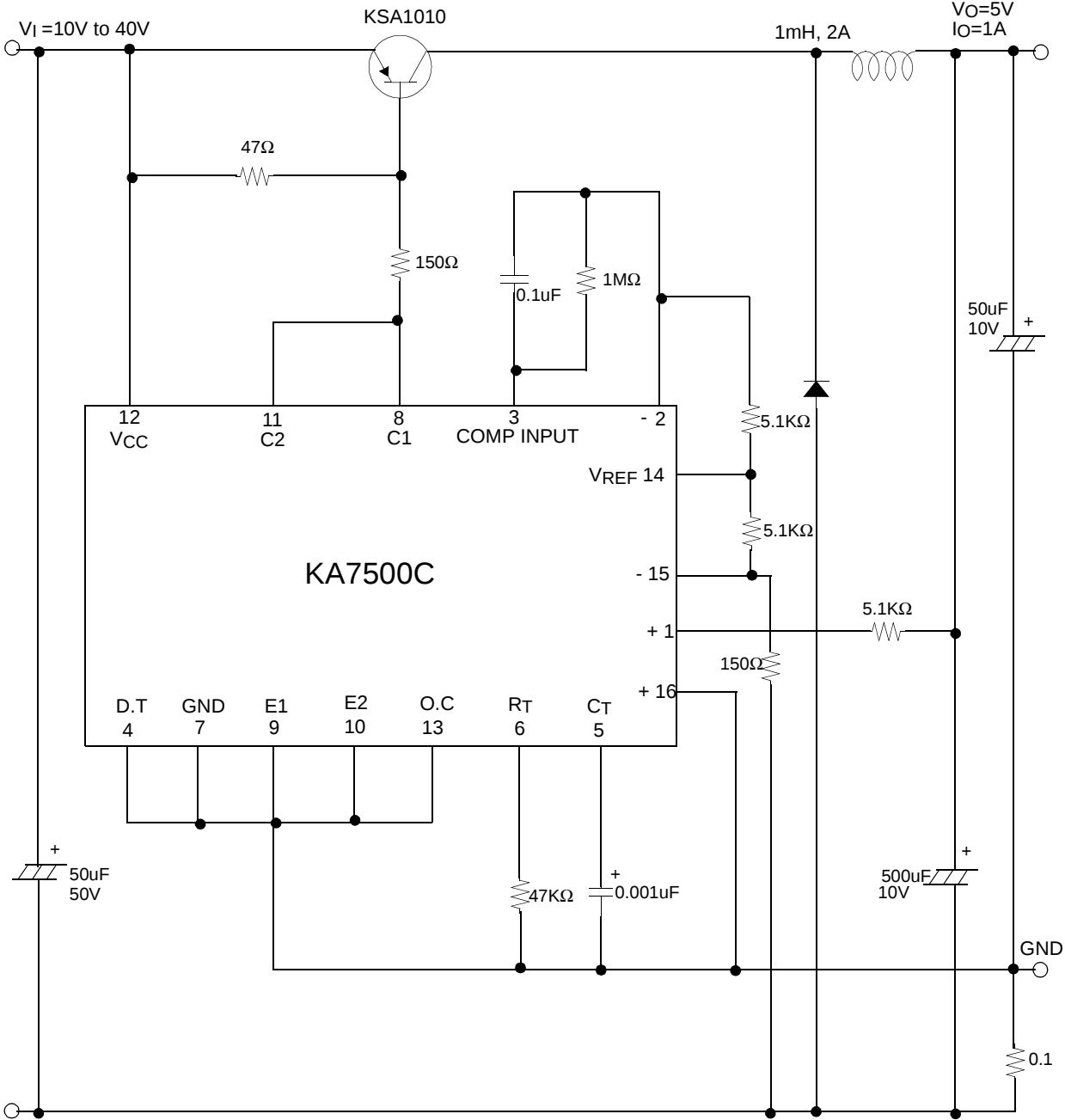
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Emitter-Follower	V _{CC(SAT)}	V _C = 15V, I _E = -200mA	-	1.5	2.5	V
Collector Off-State Current	I _{C(OFF)}	V _{CC} = 40V, V _{CE} = 40V	-	2	100	μA
Emitter Off-State Current	I _{E(OFF)}	V _{CC} = V _C = 40V, V _E = 0V	-	-	-100	-
TOTAL DEVICE						
Supply Current	I _{CC}	Pin 6 = V _{REF} , V _{CC} = 15V	-	6	10	mA
OUTPUT SWITCHING CHARACTERISTIC						
Rise Time	t _R	-	-	100	200	ns
Common Emitter, Common Collector						
Fall Time	t _F	-	-	25	100	ns
Common Emitter, Common Collector						

Note :

1. This is guaranteed where the marking code of the package surface is over 027

Typical Application

Pulse Width Modulated Step-down Converter

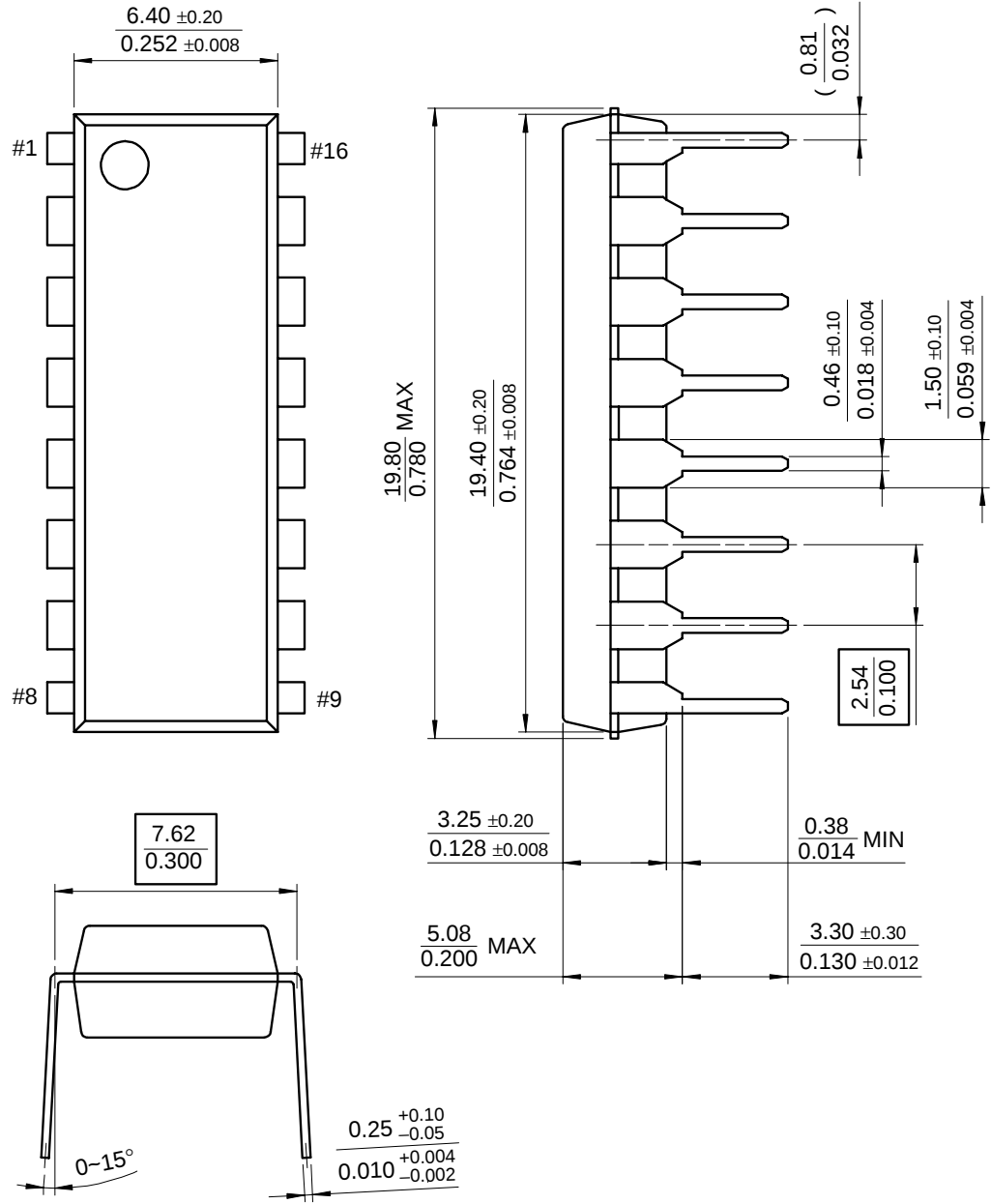


Mechanical Dimensions (Continued)

Package

Dimensions in millimeters

16-DIP



Ordering Information

Product Number	Package	Operating Temperature
KA7500C	16-DIP	-25 ~ +85°C
KA7500CD	16-SOP	

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